



Description

The HSBA3331 is the high cell density trenched P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

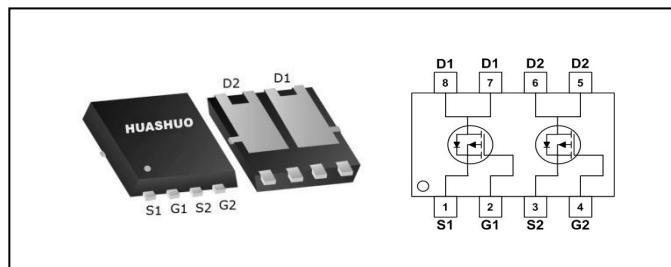
The HSBA3331 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Product Summary

V _{DS}	-30	V
R _{DSON,typ}	6.5	mΩ
I _D	-60	A

- Super Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

PRPAK5X6 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-30	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ -10V ^{1,6}	-60	A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ -10V ^{1,6}	-40	A
I _{DM}	Pulsed Drain Current ²	-200	A
EAS	Single Pulse Avalanche Energy ³	80	mJ
I _{AS}	Avalanche Current	-40	A
P _D @T _C =25°C	Total Power Dissipation ⁴	40	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹ (t≤10S)	---	35	°C/W
	Thermal Resistance Junction-ambient ¹ (Steady State)	---	55	°C/W
R _{θJC}	Thermal Resistance Junction-case ¹	---	2.2	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-30	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-10V , I _D =-20A	---	6.5	7.7	mΩ
		V _{GS} =-4.5V , I _D =-15A	---	8.2	10	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-1.2	-1.5	-2.5	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-24V , V _{GS} =0V , T _J =25°C	---	---	-1	uA
		V _{DS} =-24V , V _{GS} =0V , T _J =55°C	---	---	-5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V	---	---	±100	nA
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	1.2	---	Ω
Q _g	Total Gate Charge (-10V)	V _{DS} =-15V , V _{GS} =-10V , I _D =-18A	---	60	---	nC
Q _{gs}	Gate-Source Charge		---	9	---	
Q _{gd}	Gate-Drain Charge		---	15	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =-15V , V _{GS} =-10V , R _G =3.3Ω, I _D =-20A	---	17	---	ns
T _r	Rise Time		---	40	---	
T _{d(off)}	Turn-Off Delay Time		---	55	---	
T _f	Fall Time		---	13	---	
C _{iss}	Input Capacitance	V _{DS} =-25V , V _{GS} =0V , f=1MHz	---	3450	---	pF
C _{oss}	Output Capacitance		---	255	---	
C _{rss}	Reverse Transfer Capacitance		---	140	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V , Force Current	---	---	-60	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =-1A , T _J =25°C	---	---	-1.2	V
t _{rr}	Reverse Recovery Time	I _F =-20A , di/dt=100A/μs , T _J =25°C	---	22	---	nS
Q _{rr}	Reverse Recovery Charge		---	72	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD}=-50V,V_{GS}=-10V,L=0.1mH,I_{AS}=-40A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation
- 6.The maximum current rating is package limited.



Typical Characteristics

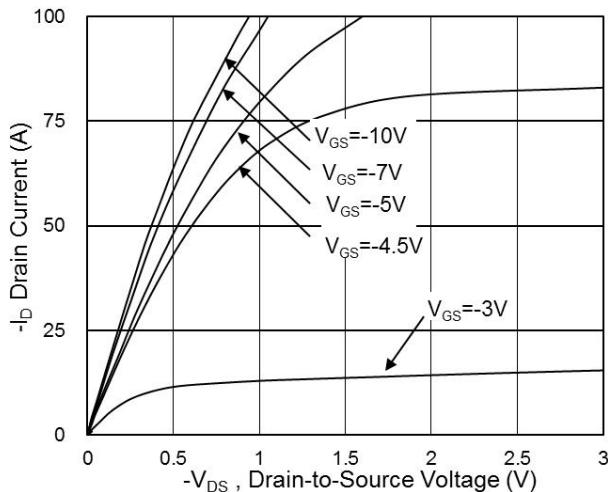


Fig.1 Typical Output Characteristics

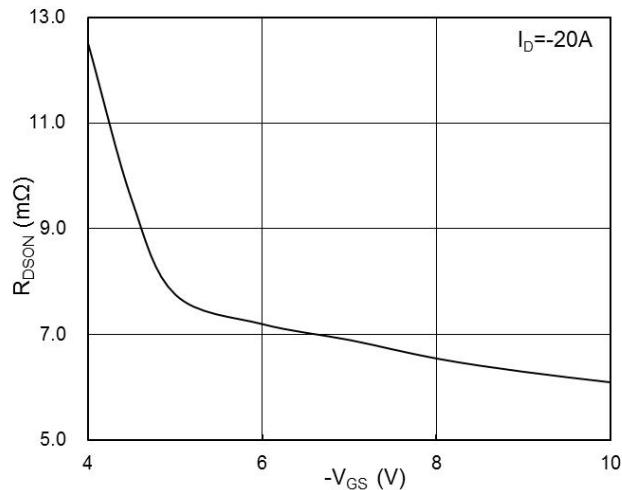


Fig.2 On-Resistance vs. Gate-Source Voltage

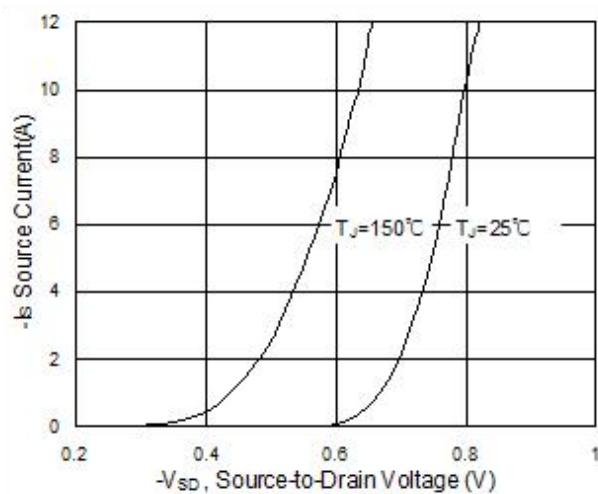


Fig.3 Forward Characteristics of Reverse

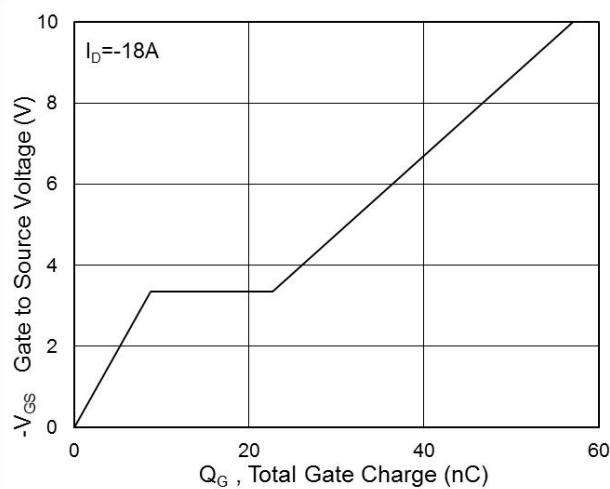


Fig.4 Gate-Charge Characteristics

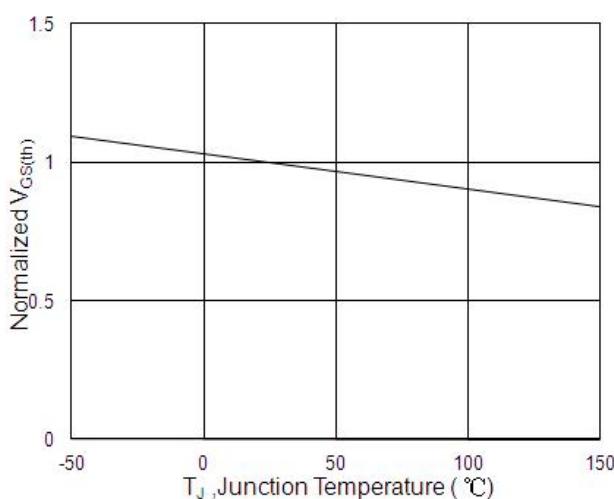


Fig.5 Normalized $-V_{GS(th)}$ vs. T_J

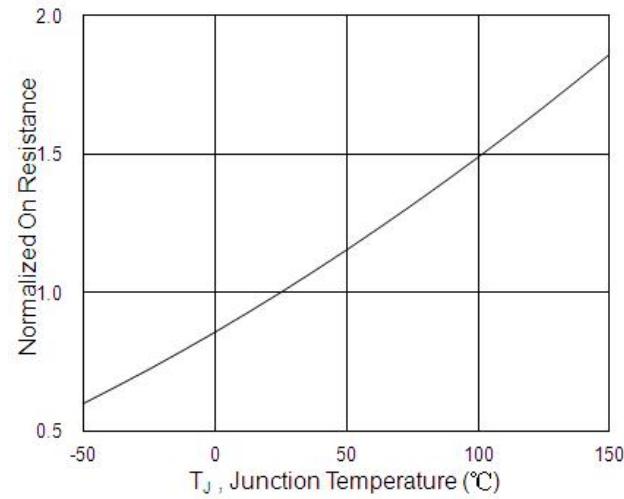


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

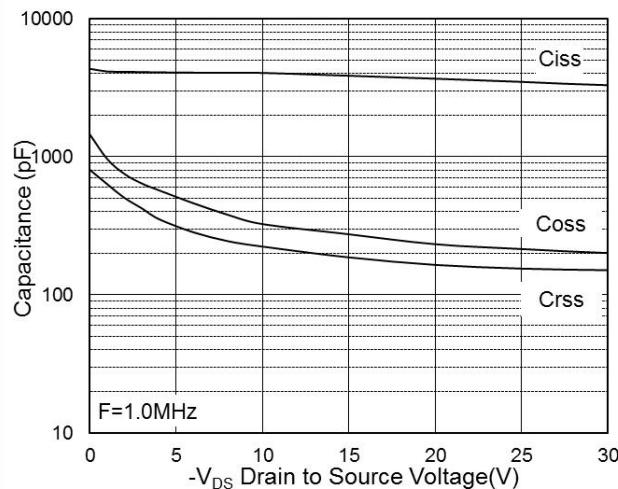


Fig.7 Capacitance

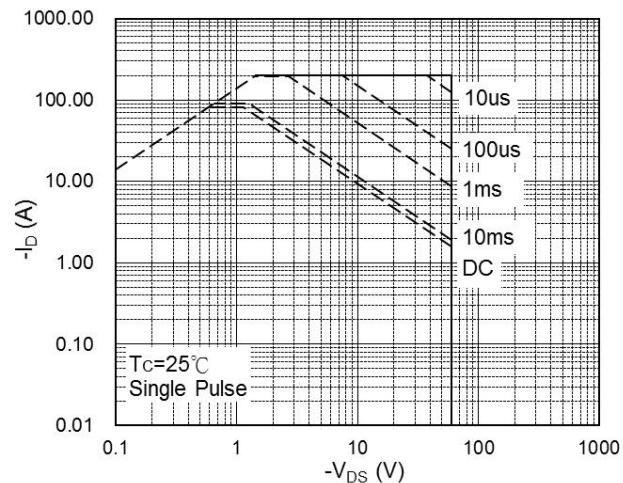


Fig.8 Safe Operating Area

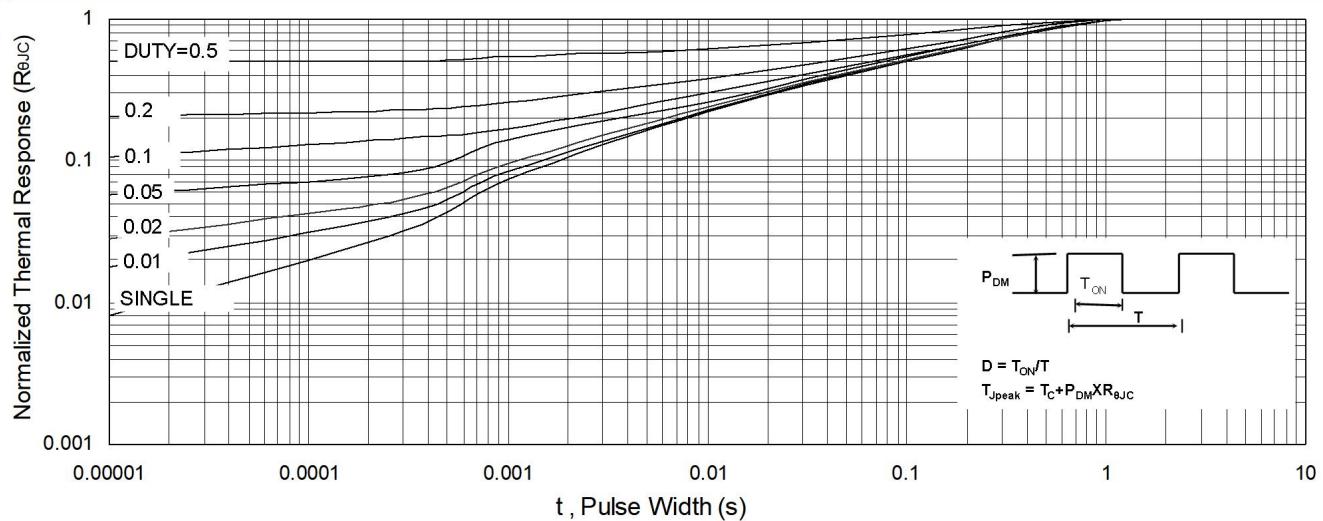


Fig.9 Normalized Maximum Transient Thermal Impedance

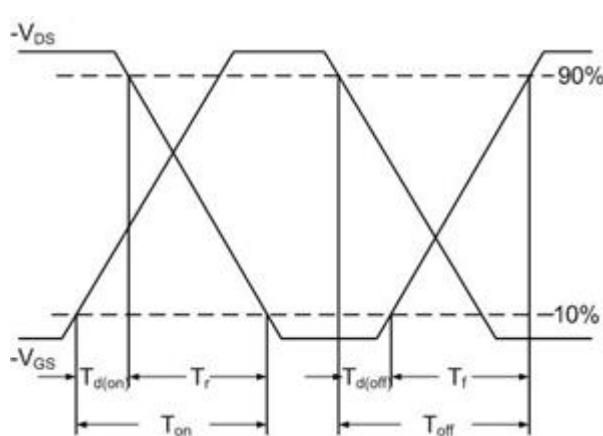


Fig.10 Switching Time Waveform

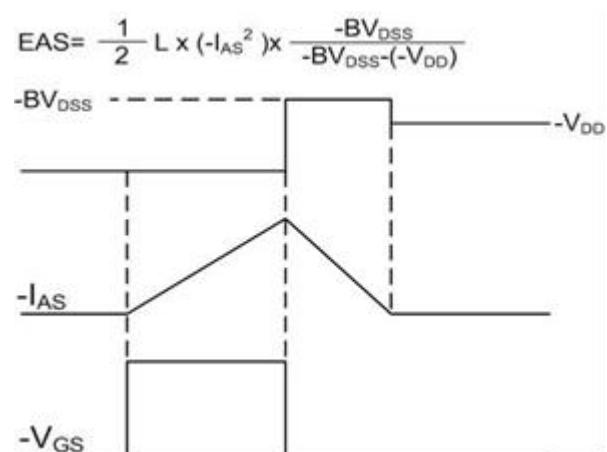
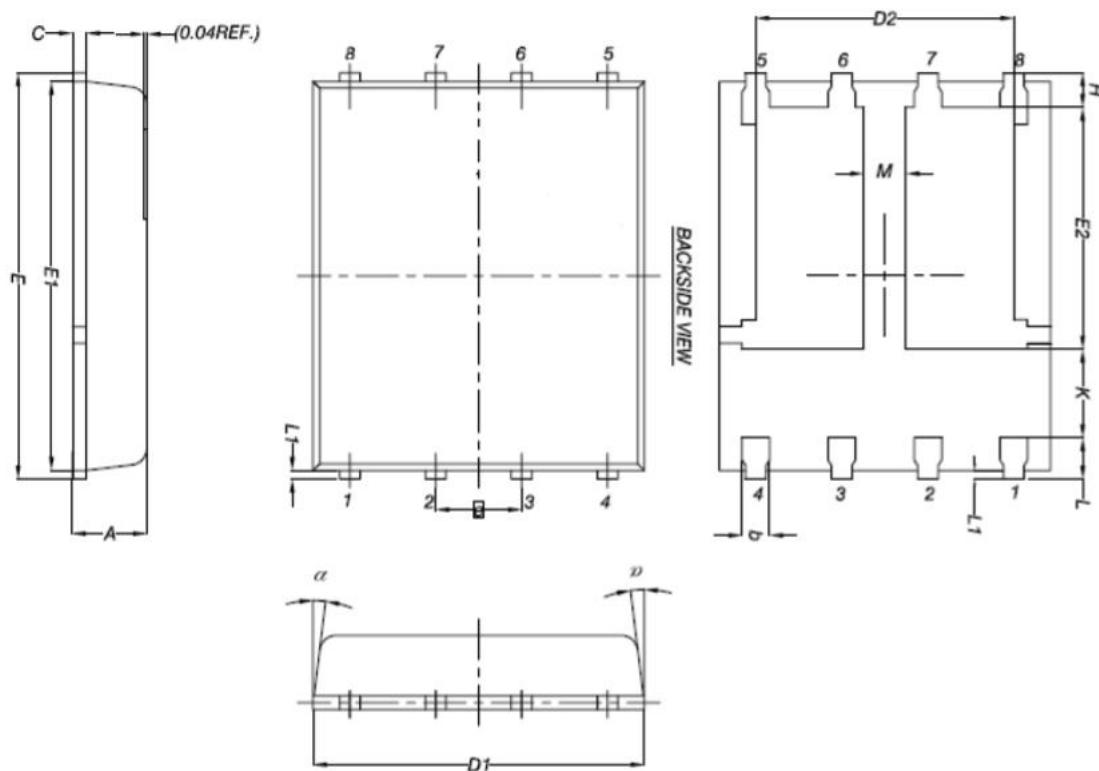


Fig.11 Unclamped Inductive Switching Waveform



PRPAK5x6-8L Dual EP2 Package Outline



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.90	1.17	0.035	0.046
b	0.33	0.51	0.013	0.020
C	0.20	0.30	0.008	0.012
D1	4.80	5.20	0.189	0.205
D2	3.61	3.96	0.142	0.156
E	5.90	6.15	0.232	0.242
E1	5.70	5.85	0.224	0.230
E2	3.30	3.78	0.130	0.149
e	1.27 BSC		0.05 BSC	
H	0.38	0.61	0.015	0.024
K	1.10	---	0.043	---
L	0.38	0.61	0.015	0.024
L1	0.05	0.25	0.002	0.010
M	0.50	---	0.020	---
α	0°	12°	0°	12°