

N-Ch 150V Fast Switching MOSFETs
General Description

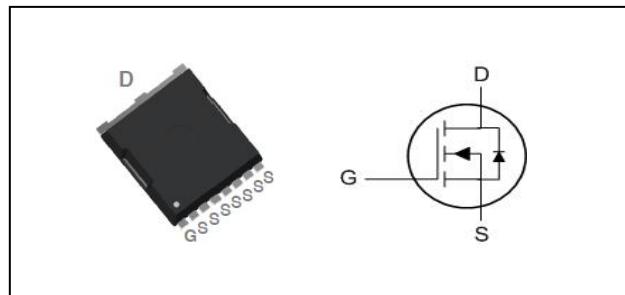
- 100% EAS Guaranteed
- Green Device Available
- Super Low RDS(ON)
- Advanced high cell density Trench technology

Product Summary

V _{DS}	150	V
R _{DS(ON),typ}	3.5	mΩ
I _D	255	A

Applications

- MOTOR Driver.
- UPS.
- Power Tools.
- Synchronous Rectification in SMPS.

TOLL Pin Configuration

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	150	V
V _{GS}	Gate-Source Voltage	± 20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ^{1,6}	255	A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ^{1,6}	160	A
I _{DM}	Pulsed Drain Current ²	1020	A
EAS	Single Pulse Avalanche Energy ³	1300	mJ
P _D @T _C =25°C	Total Power Dissipation ⁴	600	W
T _{STG}	Storage Temperature Range	-55 to 175	°C
T _J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	50	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	0.25	°C/W

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Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

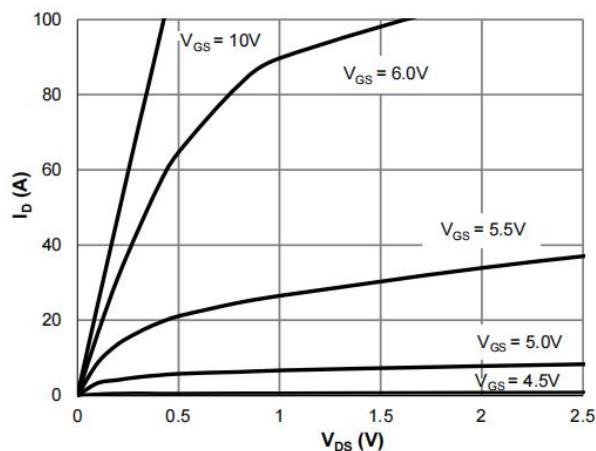
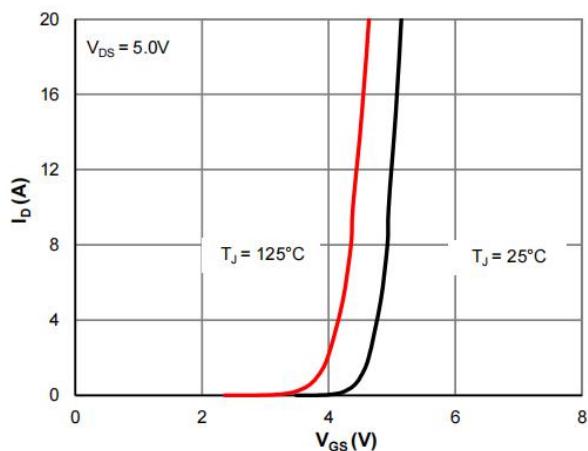
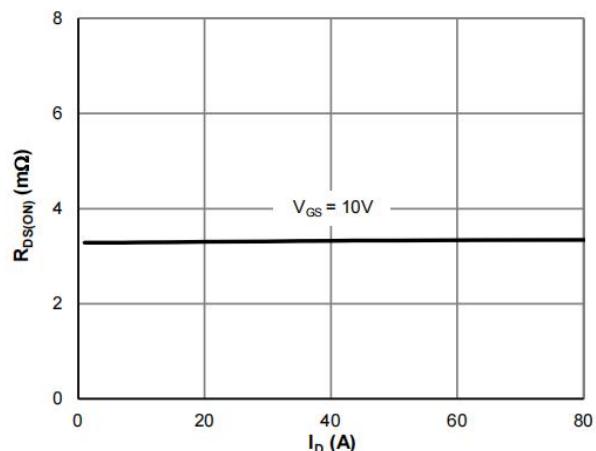
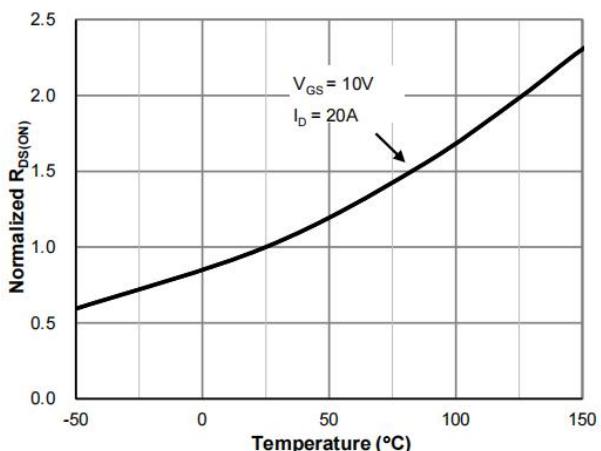
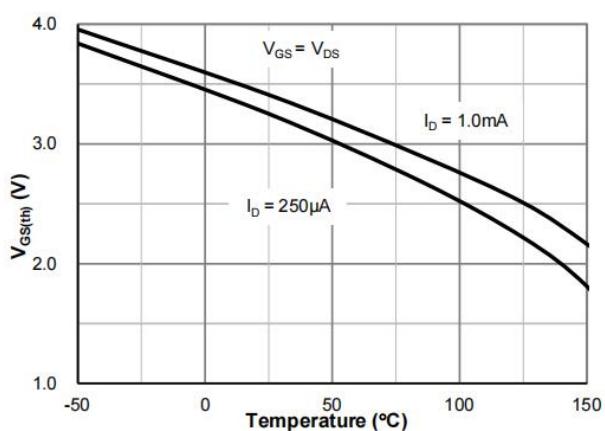
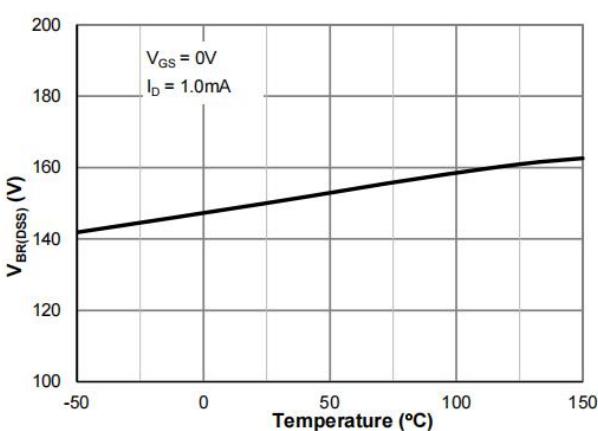
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_D=250\mu\text{A}$	150	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=20\text{A}$	---	3.5	4.0	$\text{m}\Omega$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$, $\text{I}_D=250\mu\text{A}$	2.0	3.0	4.0	V
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=120\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $\text{T}_J=25^{\circ}\text{C}$	---	---	1	μA
		$\text{V}_{\text{DS}}=120\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $\text{T}_J=55^{\circ}\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$	---	---	± 100	nA
R_g	Gate Resistance	$\text{V}_{\text{DS}}=0\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	4.9	---	Ω
Q_g	Total Gate Charge (10V)	$\text{V}_{\text{DS}}=75\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=20\text{A}$	---	128	---	nC
Q_{gs}	Gate-Source Charge		---	83	---	
Q_{gd}	Gate-Drain Charge		---	44	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$\text{V}_{\text{DD}}=75\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{R}_g=4.5\Omega$, $\text{R}_L=1\Omega$, $\text{I}_D=20\text{A}$	---	33	---	ns
T_r	Rise Time		---	68	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	85	---	
T_f	Fall Time		---	41	---	
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=75\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	8660	---	pF
C_{oss}	Output Capacitance		---	720	---	
Crss	Reverse Transfer Capacitance		---	28	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{SD}	Diode Forward Voltage ²	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_S=50\text{A}$, $\text{T}_J=25^{\circ}\text{C}$	---	---	1.1	V
t_{rr}	Reverse Recovery Time	$\text{I}_F=15\text{A}$, $d\text{I}/dt=100\text{A}/\mu\text{s}$, $\text{T}_J=25^{\circ}\text{C}$	---	118	---	nS
Q_{rr}	Reverse Recovery Charge		---	431	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The EAS data shows Max. rating . The test condition is $\text{V}_{\text{DD}}=75\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $L=0.3\text{mH}$, $\text{I}_{\text{AS}} = 75\text{A}$.
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.
- 6.Package limitation current.

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Typical Characteristics

Figure 1: Saturation Characteristics

Figure 2: Transfer Characteristics

Figure 3: $R_{DS(ON)}$ vs. Drain Current

Figure 4: $R_{DS(ON)}$ vs. Junction Temperature

Figure 5: $V_{GS(th)}$ vs. Junction Temperature

Figure 6: $V_{BR(DSS)}$ vs. Junction Temperature



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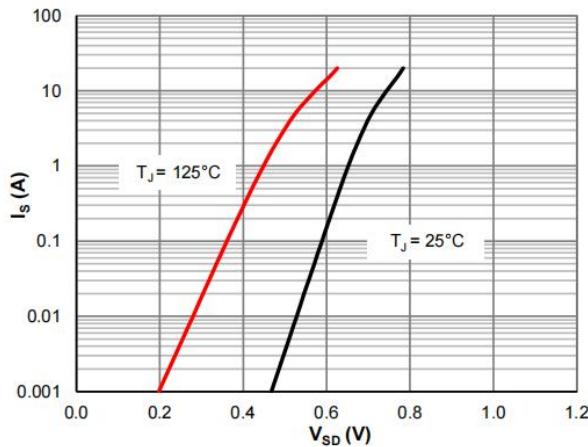


Figure 7: Body-Diode Characteristics

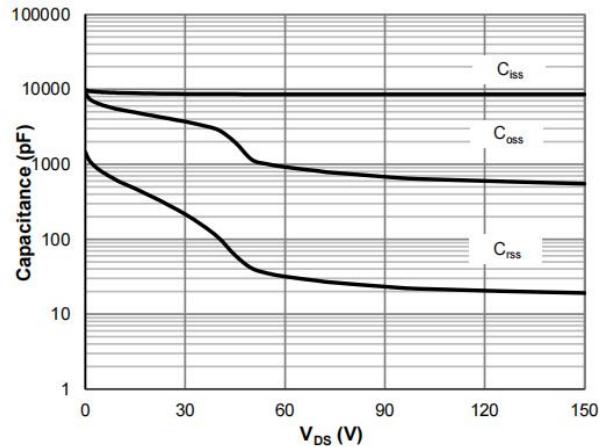


Figure 8: Capacitance Characteristics

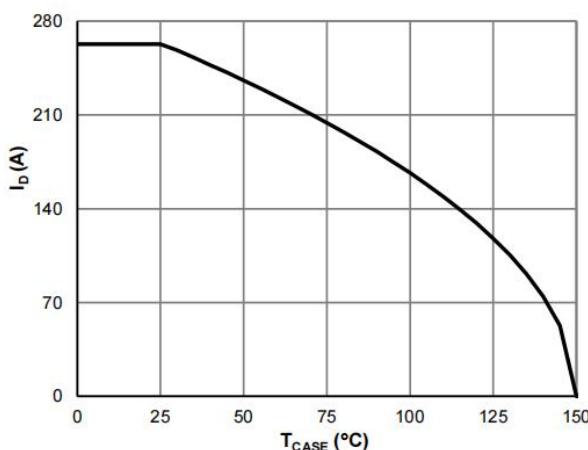


Figure 9: Current De-rating

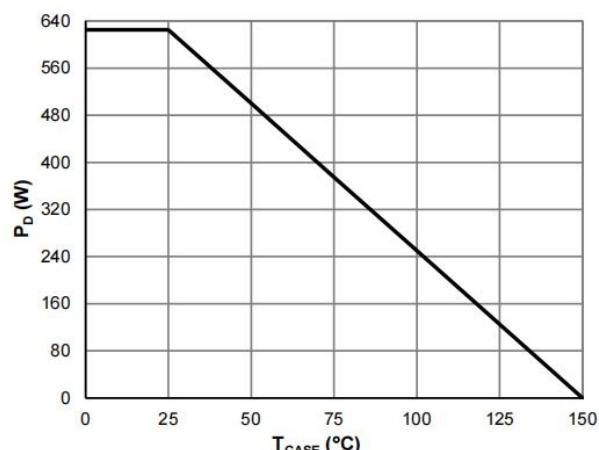


Figure 10: Power De-rating

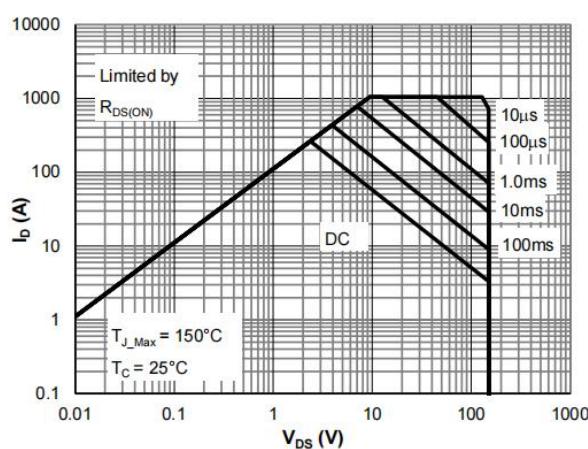


Figure 11: Maximum Safe Operating Area

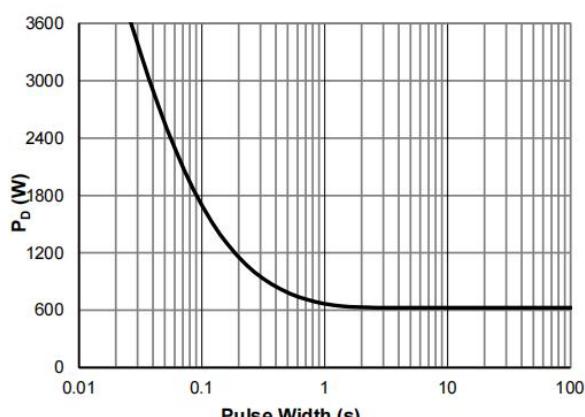


Figure 12: Single Pulse Power Rating, Junction-to-Case

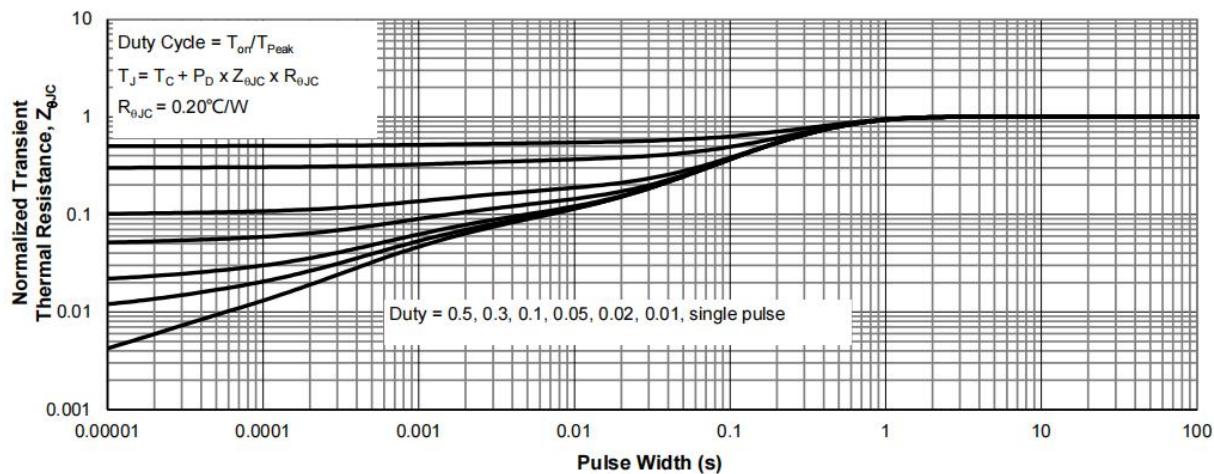
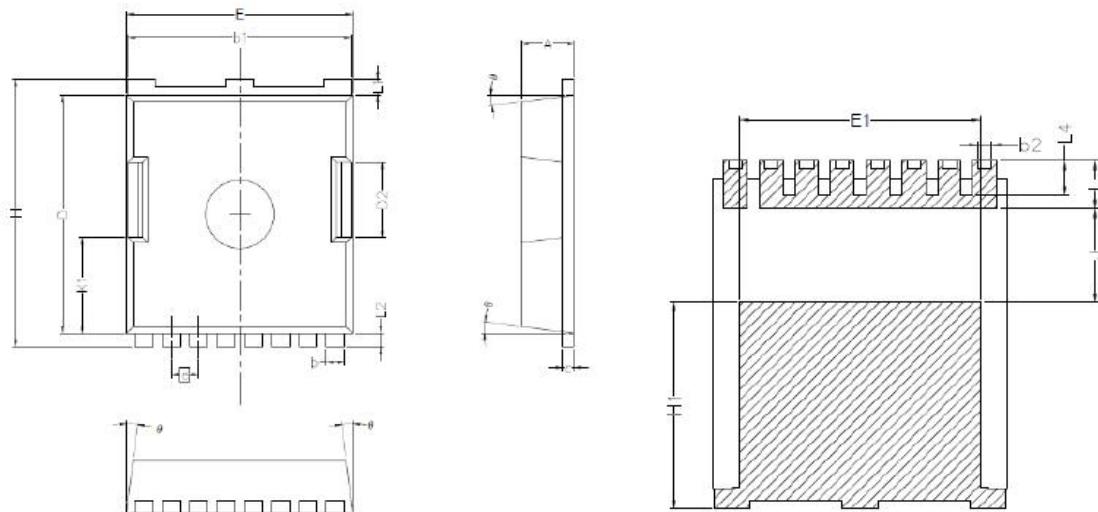


Figure 13: Normalized Maximum Transient Thermal Impedance



■ TOLL-8L Package information



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	2.20	2.40
b	0.90	0.90
b1	9.70	9.90
b2	0.42	0.50
c	0.40	0.60
D	10.28	10.58
D2	3.10	3.50
E	9.70	10.10
E1	7.90	8.30
e	1.20BSC	
H	11.48	11.88
H1	6.75	7.15
N	8	
J	3.00	3.30
K1	3.98	4.38
L	1.40	1.80
L1	0.60	0.80
L2	0.50	0.70
L4	1.00	1.30
θ	4°	10°