



**N-Ch 100V Fast Switching MOSFETs**

**Applications**

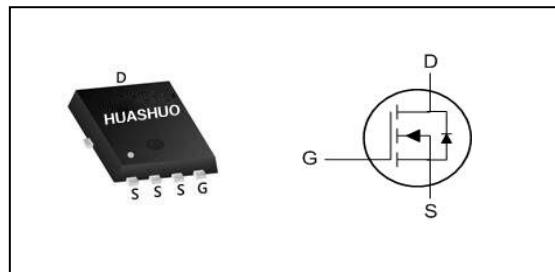
Portable Equipment.  
Battery Powered Systems.  
Hard Switching and High-Speed Circuit.

**Product Summary**

$V_{DS}$	100	V
$R_{DS(ON),TYP}$	25	$m\Omega$
$I_D$	25	A

- 100% EAS Guaranteed
- Low  $R_{DS(ON)}$
- Low Gate Charge
- RoHs and Halogen-Free Compliant

**PRPAK3\*3 Pin Configuration**



**Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D@T_c=25^\circ C$	Continuous Drain Current <sup>1</sup>	25	A
$I_D@T_c=100^\circ C$	Continuous Drain Current <sup>1</sup>	15	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	100	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	60	mJ
$P_D@T_c=25^\circ C$	Total Power Dissipation <sup>4</sup>	28	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

**Thermal Data**

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	---	55	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	4.5	°C/W



**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_D=250\mu\text{A}$	100	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>2</sup>	$\text{V}_{\text{GS}}=10\text{V}$ , $\text{I}_D=10\text{A}$	---	25	30	$\text{m}\Omega$
	Static Drain-Source On-Resistance <sup>2</sup>	$\text{V}_{\text{GS}}=4.5\text{V}$ , $\text{I}_D=10\text{A}$	---	30	40	
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$ , $\text{I}_D=250\mu\text{A}$	1.0	1.5	2.5	V
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=100\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\text{uA}$
		$\text{V}_{\text{DS}}=100\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $T_J=55^\circ\text{C}$	---	---	5	
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}$ , $\text{V}_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$\text{R}_g$	Gate Resistance	$\text{V}_{\text{DS}}=0\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	0.6	---	$\Omega$
$\text{Q}_g$	Total Gate Charge (10V)	$\text{V}_{\text{DS}}=50\text{V}$ , $\text{V}_{\text{GS}}=10\text{V}$ , $\text{I}_D=10\text{A}$	---	16	---	$\text{nC}$
$\text{Q}_{\text{gs}}$	Gate-Source Charge		---	4.8	---	
$\text{Q}_{\text{gd}}$	Gate-Drain Charge		---	2.2	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$\text{V}_{\text{DD}}=30\text{V}$ , $\text{V}_{\text{GS}}=10\text{V}$ , $\text{R}_g=6\Omega$ , $\text{I}_D=1\text{A}$	---	7	---	$\text{ns}$
$\text{T}_r$	Rise Time		---	8	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	21	---	
$\text{T}_f$	Fall Time		---	3	---	
$\text{C}_{\text{iss}}$	Input Capacitance	$\text{V}_{\text{DS}}=50\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	590	---	$\text{pF}$
$\text{C}_{\text{oss}}$	Output Capacitance		---	175	---	
$\text{Crss}$	Reverse Transfer Capacitance		---	9	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{I}_s$	Continuous Source Current <sup>1,5,6</sup>	$\text{V}_G=\text{V}_D=0\text{V}$ , Force Current	---	---	25	A
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.2	V
$\text{t}_{\text{rr}}$	Reverse Recovery Time	$\text{IF}=10\text{A}$ , $d\text{I}/dt=100\text{A}/\mu\text{s}$ , $T_J=25^\circ\text{C}$	---	30	---	nS
	Reverse Recovery Charge		---	135	---	nC

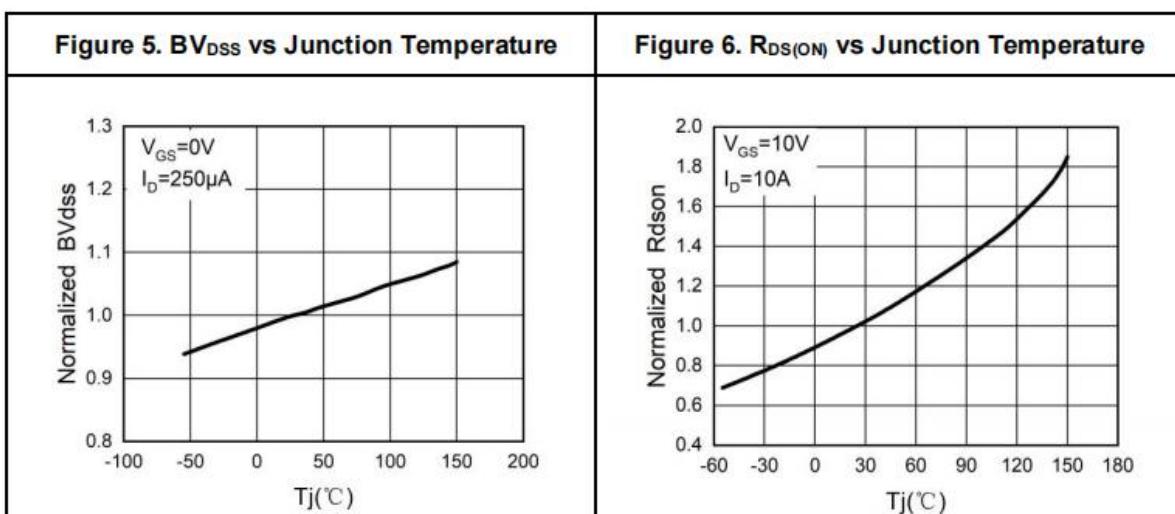
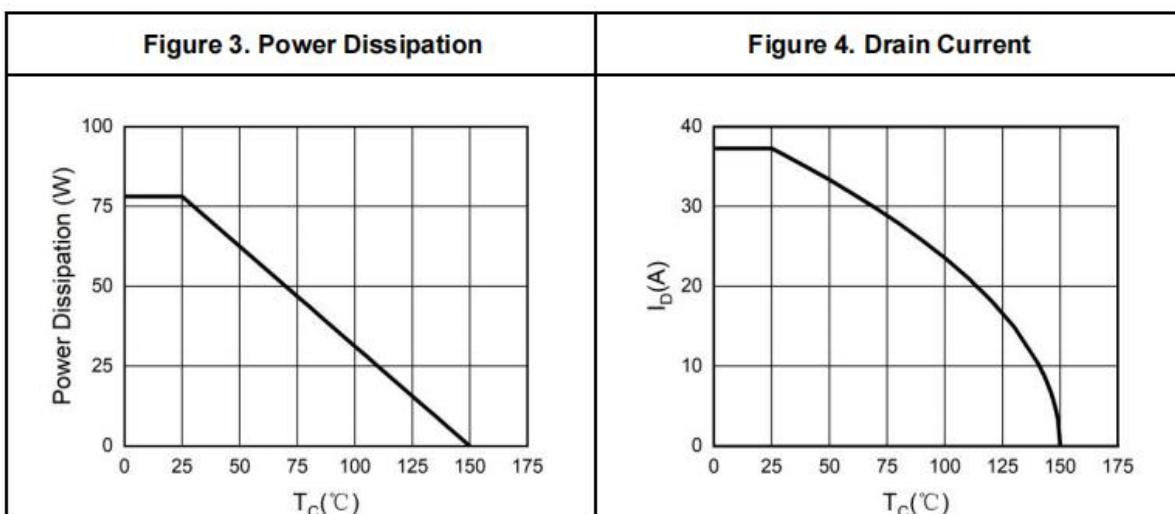
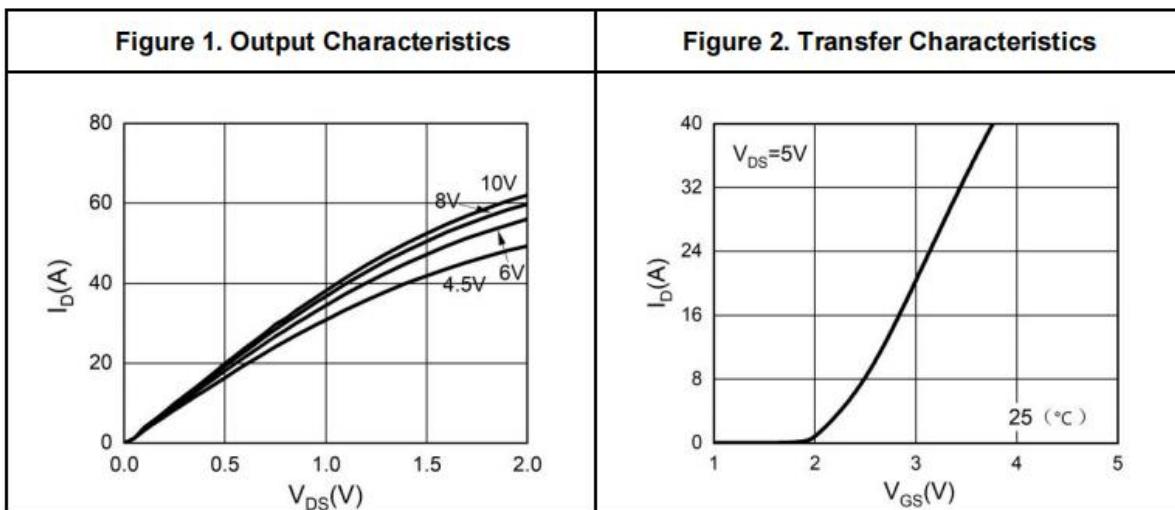
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $\text{V}_{\text{DD}}=25\text{V}$ ,  $\text{V}_{\text{GS}}=10\text{V}$ ,  $\text{L}=0.5\text{mH}$
- 4.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 5.The data is theoretically the same as  $\text{I}_D$  and  $\text{I}_{\text{DM}}$  , in real applications , should be limited by total power dissipation.
- 6.The maximum current rating is package limited.



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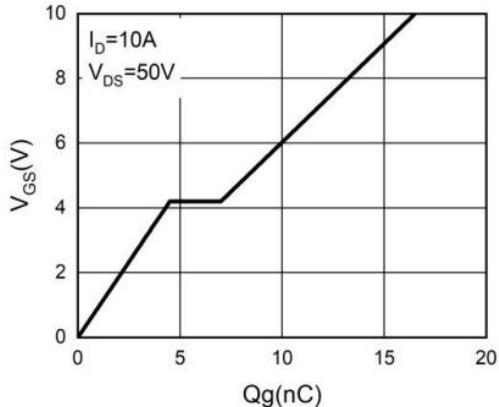
Typical Characteristics



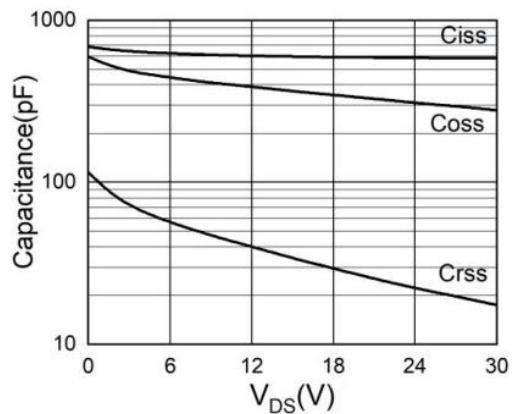


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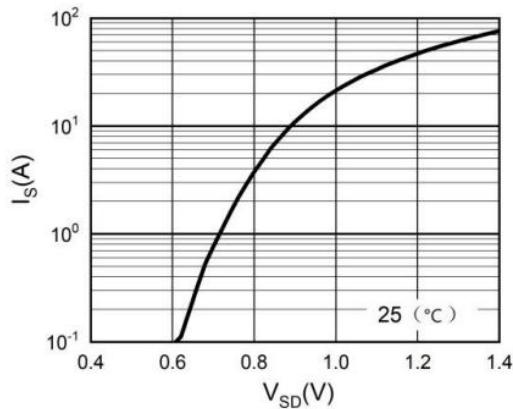
**Figure 7. Gate Charge Waveforms**



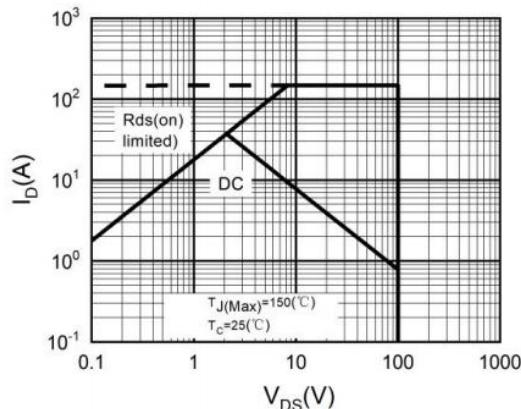
**Figure 8. Capacitance**



**Figure 9. Body-Diode Characteristics**



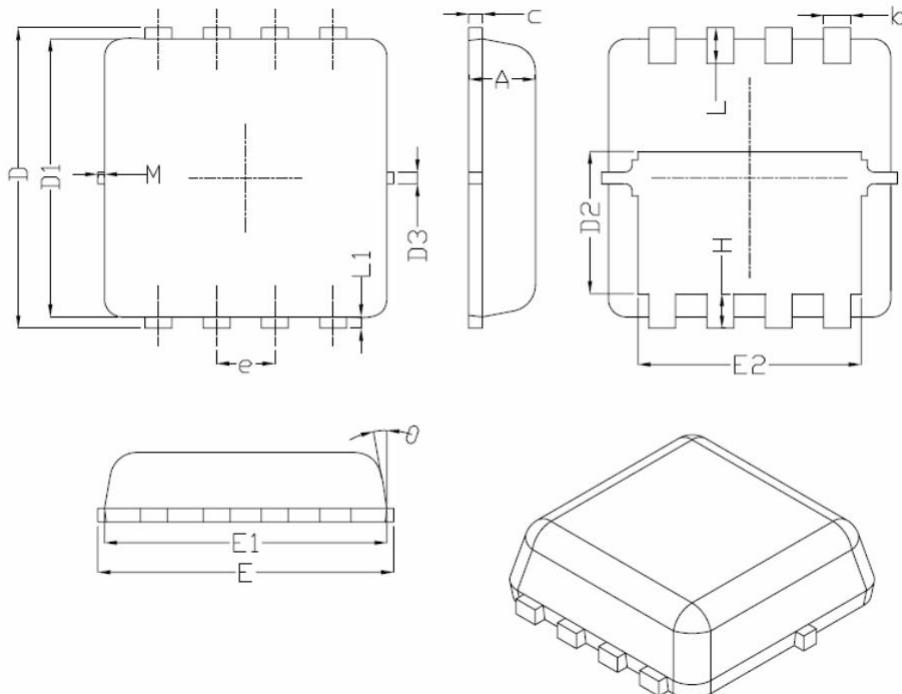
**Figure 10. Maximum Safe Operating Area**





## Ordering Information

Part Number	Package code	Packaging
HSBB0098	PRPAK3*3	3000/Tape&Reel



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.48	1.58	1.68
D3	-	0.13	-
E	3.15	3.30	3.45
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	-
M	*	*	0.15
θ		10°	12°