



General Description

- 100% UIS Tested
- Advanced Trench Technology
- Low Gate Charge
- High Current Capability
- RoHS and Halogen-Free Compliant

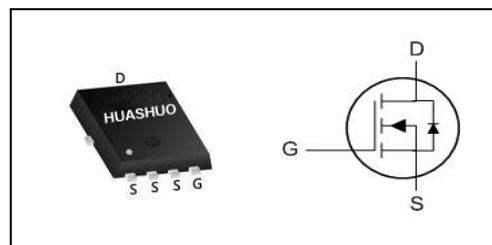
Product Summary

V_{DS}	40	V
$R_{DS(ON),Max}$	2.2	$m\Omega$
I_D	110	A

Applications

- SMPS Synchronous Rectification
- DC/DC Converters
- Or-ing

PRPAK5X6 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	110	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	80	A
I_{DM}	Pulsed Drain Current ²	440	A
EAS	Single Pulse Avalanche Energy ³	380	mJ
I_{AS}	Avalanche Current	50	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation ⁴	100	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	55	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	1.3	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	40	---	---	V
R _{DSON}	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =20A	---	---	2.2	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	2	---	4	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =40V , V _{GS} =0V , T _J =25°C	---	---	1	uA
		V _{DS} =40V , V _{GS} =0V , T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ± 20V , V _{DS} =0V	---	---	± 100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V , I _D =20A	---	61	---	S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	1.9	---	Ω
Q _g	Total Gate Charge	V _{DS} =15V , V _{GS} =10V , I _D =20A	---	52	---	nC
Q _{gs}	Gate-Source Charge		---	11	---	
Q _{gd}	Gate-Drain Charge		---	7.5	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =15V , V _{GS} =10V , R _G =3.3Ω , I _D =20A	---	16	---	ns
T _r	Rise Time		---	7.5	---	
T _{d(off)}	Turn-Off Delay Time		---	48	---	
T _f	Fall Time		---	13.6	---	
C _{iss}	Input Capacitance	V _{DS} =20V , V _{GS} =0V , f=1MHz	---	3572	---	pF
C _{oss}	Output Capacitance		---	1719	---	
C _{rss}	Reverse Transfer Capacitance		---	102	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,6}	V _G =V _D =0V , Force Current	---	---	110	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25°C	---	---	1.2	V
t _{rr}	Reverse Recovery Time	I _F =20A , dI/dt=100A/μs , T _J =25°C	---	44	---	nS
Q _{rr}	Reverse Recovery Charge		---	33	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD}=25V,V_{GS}=10V,L=0.5mH,I_{AS}=50A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.
- 6.Package limitation current is 100A.



Typical Characteristics

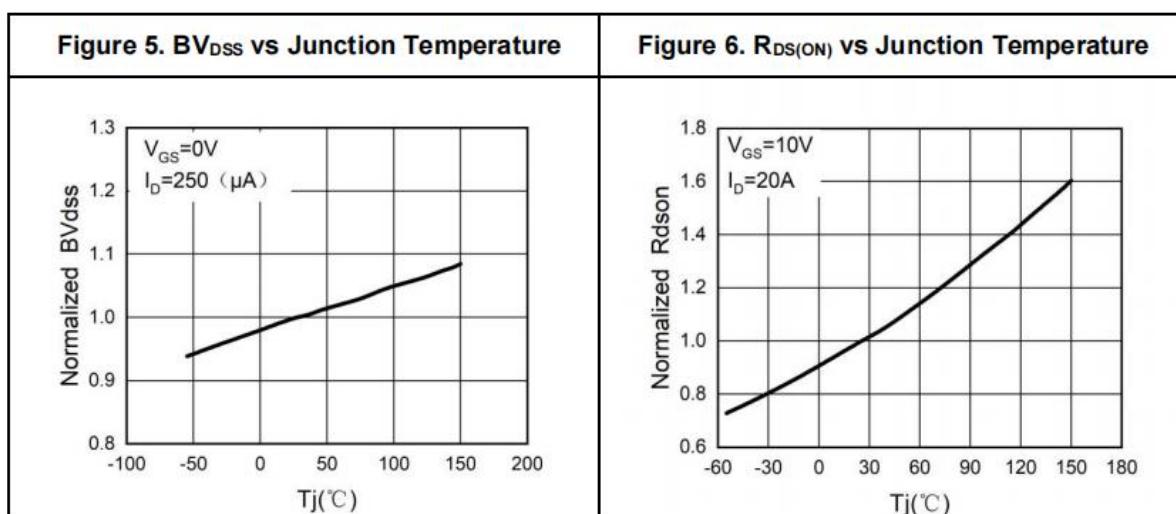
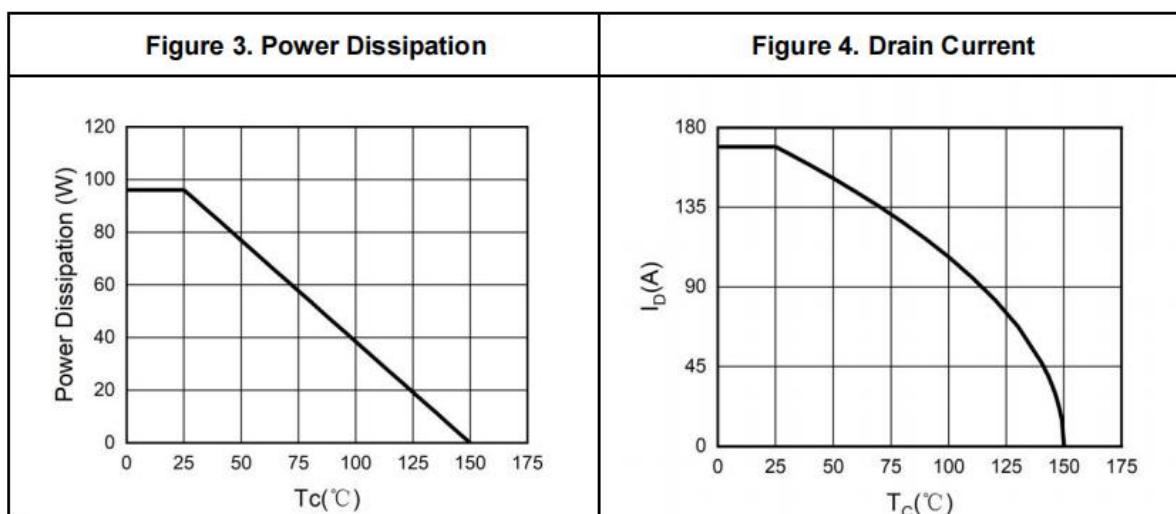
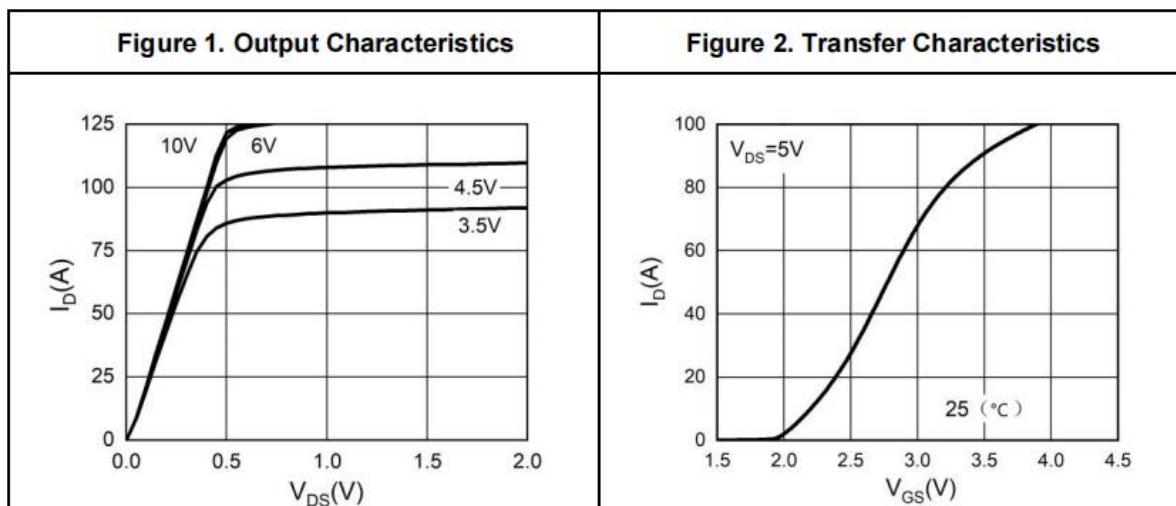




Figure 7. Gate Charge Waveforms

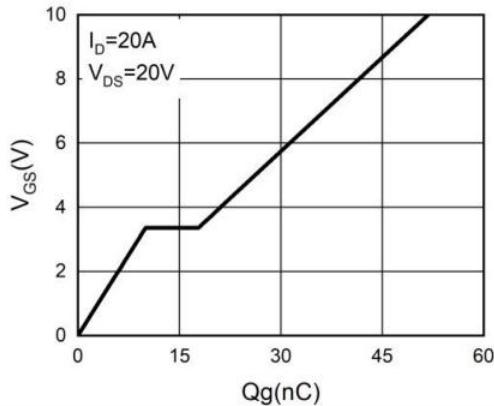


Figure 8. Capacitance

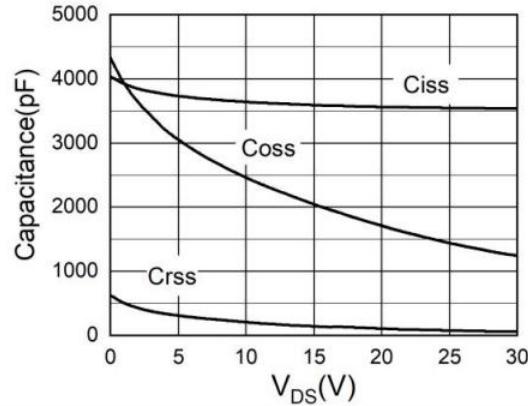


Figure 9. Body-Diode Characteristics

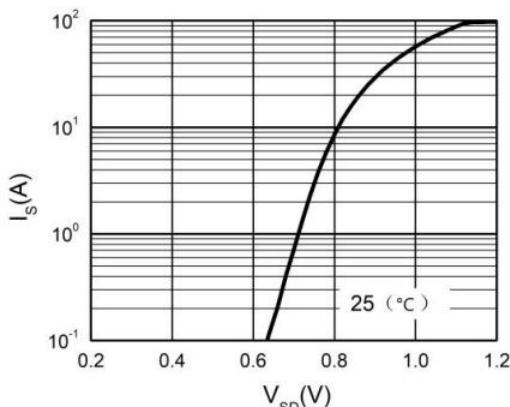
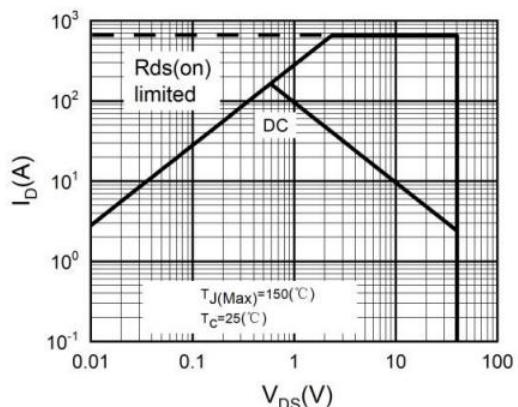


Figure 10. Maximum Safe Operating Area



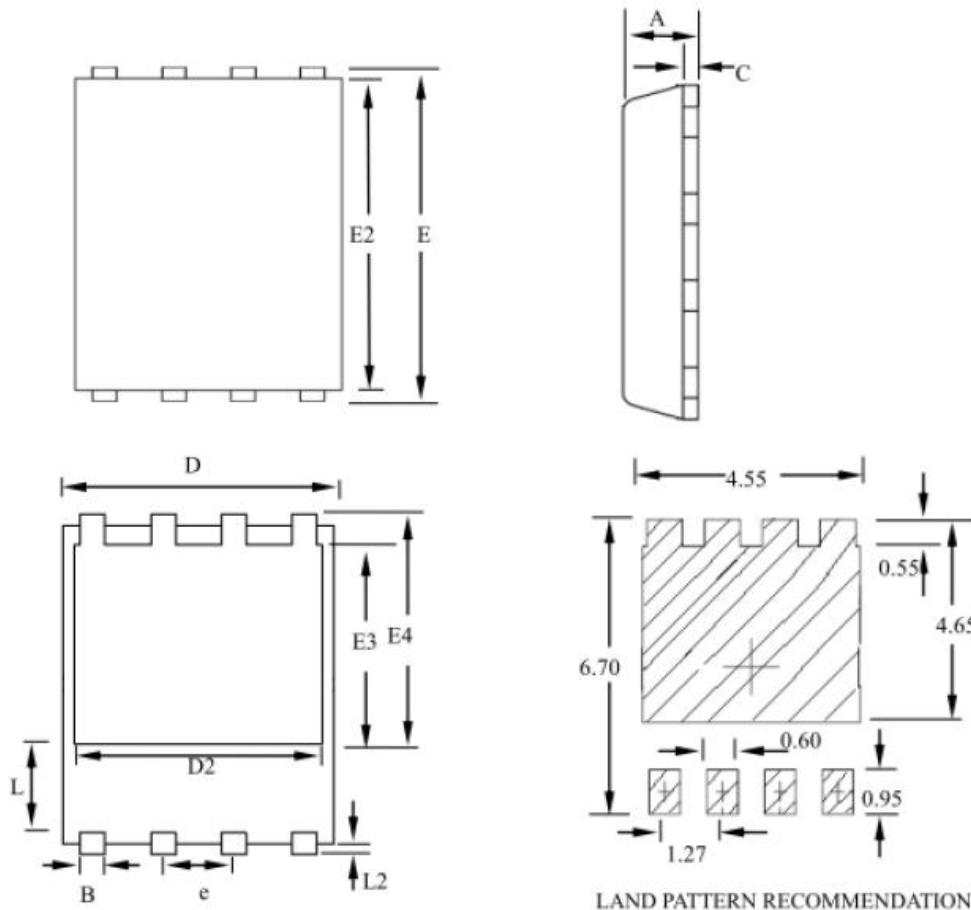


HUASHUO
SEMICONDUCTOR

HSBA4050A

N-Ch 40V Fast Switching MOSFETs

PRPAK5X6 Package Outline Dimensions



SYMBOLS	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	--	1.20	0.031	--	0.047
B	0.30	--	0.51	0.012	--	0.020
C	0.15	--	0.35	0.006	--	0.014
D	4.80	--	5.30	0.189	--	0.209
D2	3.61	--	4.35	0.142	--	0.171
E	5.90	--	6.35	0.232	--	0.250
E2	5.42	--	5.90	0.213	--	0.232
E3	3.23	--	3.90	0.127	--	0.154
E4	3.69	--	4.55	0.145	--	0.179
L	0.61	--	1.80	0.024	--	0.071
L2	0.05	--	0.36	0.002	--	0.014
e	--	1.27	--	--	0.050	--