



### General Description

- 100% UIS Tested
- Advanced Trench Technology
- Low Gate Charge
- High Current Capability
- RoHS and Halogen-Free Compliant

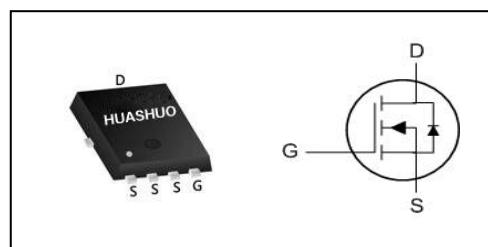
### Product Summary

V <sub>DS</sub>	40	V
R <sub>DSON,typ</sub>	1.6	mΩ
I <sub>D</sub>	140	A

### Applications

- SMPS Synchronous Rectification
- DC/DC Converters
- Or-ing

### PRPAK5X6 Pin Configuration



### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	40	V
V <sub>GS</sub>	Gate-Source Voltage	± 20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1,6</sup>	140	A
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1,6</sup>	90	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	560	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	380	mJ
I <sub>AS</sub>	Avalanche Current	50	A
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	100	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-Ambient <sup>1</sup>	---	55	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	---	1.3	°C/W

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	40	---	---	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =20A	---	1.6	1.8	mΩ
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =20A	---	2.2	2.6	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.2	1.8	2.5	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =40V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =40V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C	---	---	5	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ± 20V , V <sub>DS</sub> =0V	---	---	± 100	nA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =20A	---	61	---	S
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz	---	1.9	---	Ω
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =15V , V <sub>GS</sub> =10V , I <sub>D</sub> =20A	---	52	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	11	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	7.5	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =15V , V <sub>GS</sub> =10V , R <sub>G</sub> =3.3Ω , I <sub>D</sub> =20A	---	16	---	ns
T <sub>r</sub>	Rise Time		---	7.5	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	48	---	
T <sub>f</sub>	Fall Time		---	13.6	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =20V , V <sub>GS</sub> =0V , f=1MHz	---	3572	---	pF
C <sub>oss</sub>	Output Capacitance		---	1719	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	102	---	

**Diode Characteristics**

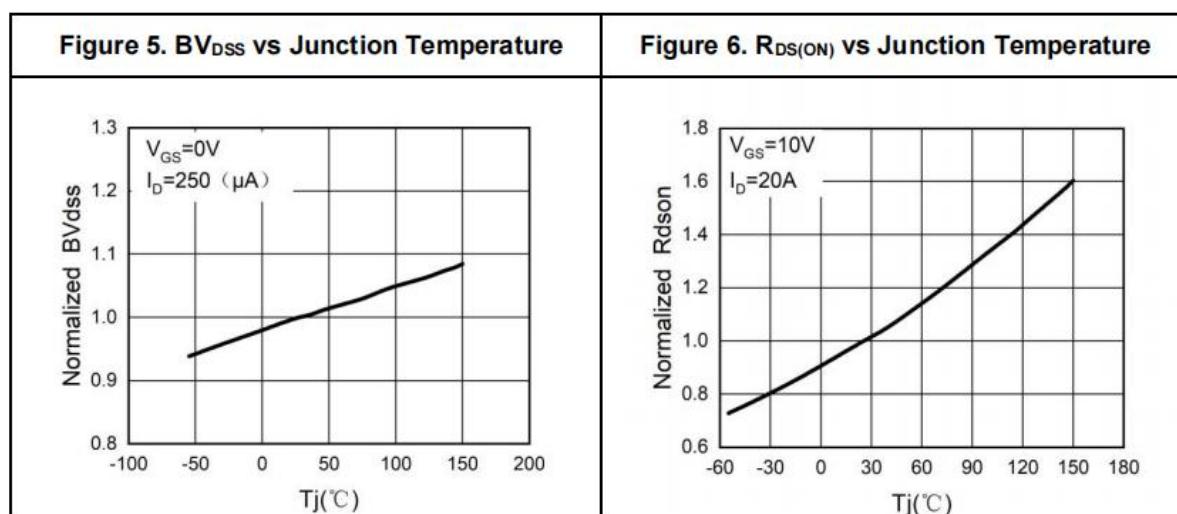
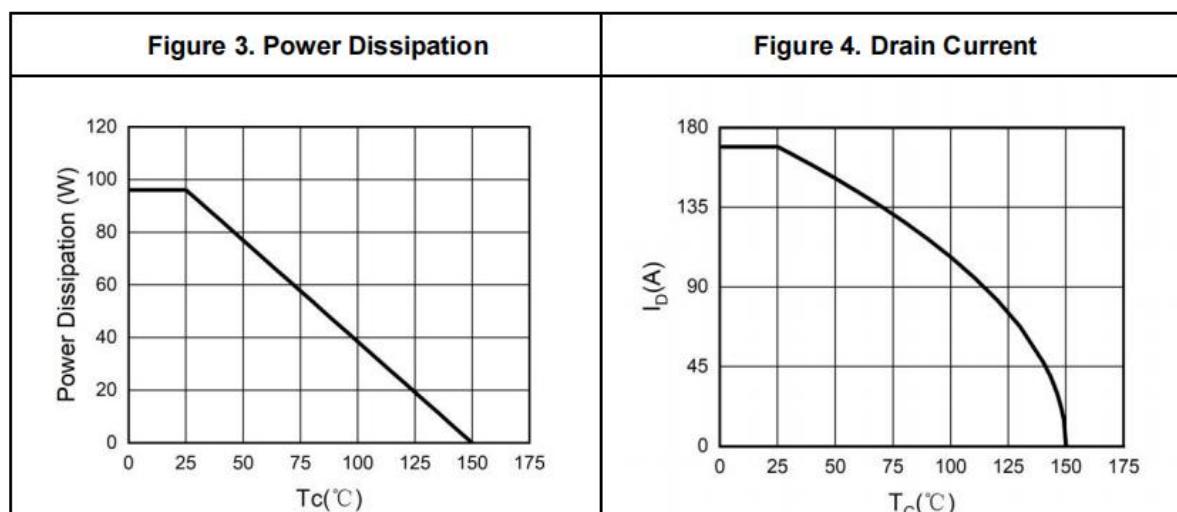
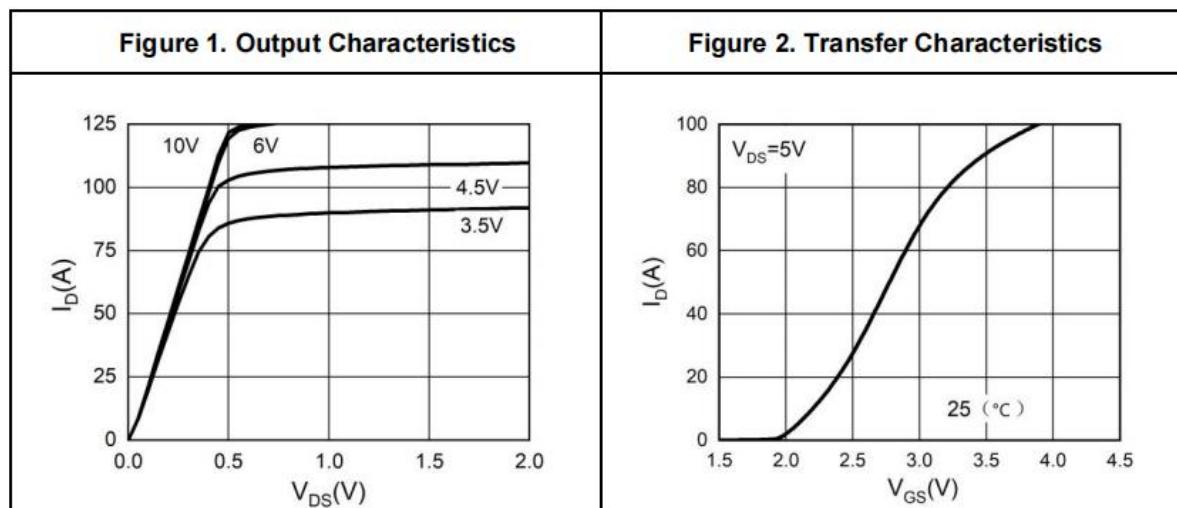
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current	---	---	140	A
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C	---	---	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	I=20A , dI/dt=100A/μs , T <sub>J</sub> =25°C	---	44	---	nS
Q <sub>rr</sub>	Reverse Recovery Charge		---	33	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=25V,V<sub>GS</sub>=10V,L=0.5mH,I<sub>AS</sub>=50A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.
- 6.Package limitation current is 100A.

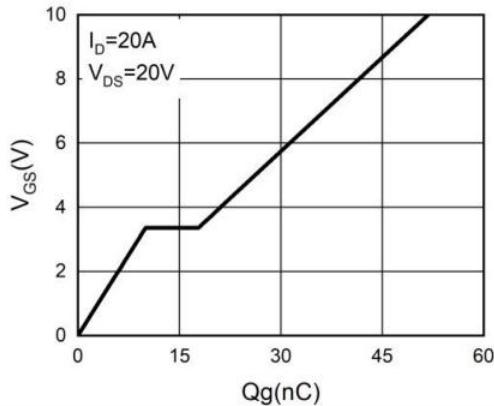


**Typical Characteristics**

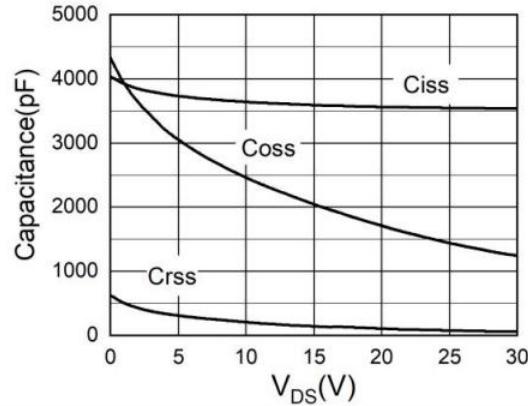




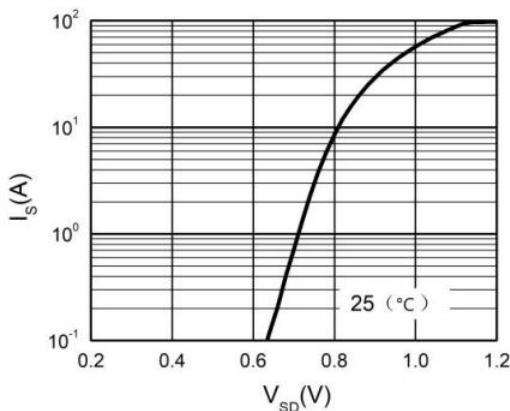
**Figure 7. Gate Charge Waveforms**



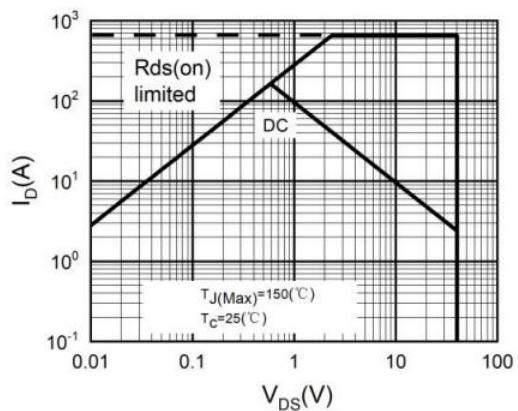
**Figure 8. Capacitance**



**Figure 9. Body-Diode Characteristics**

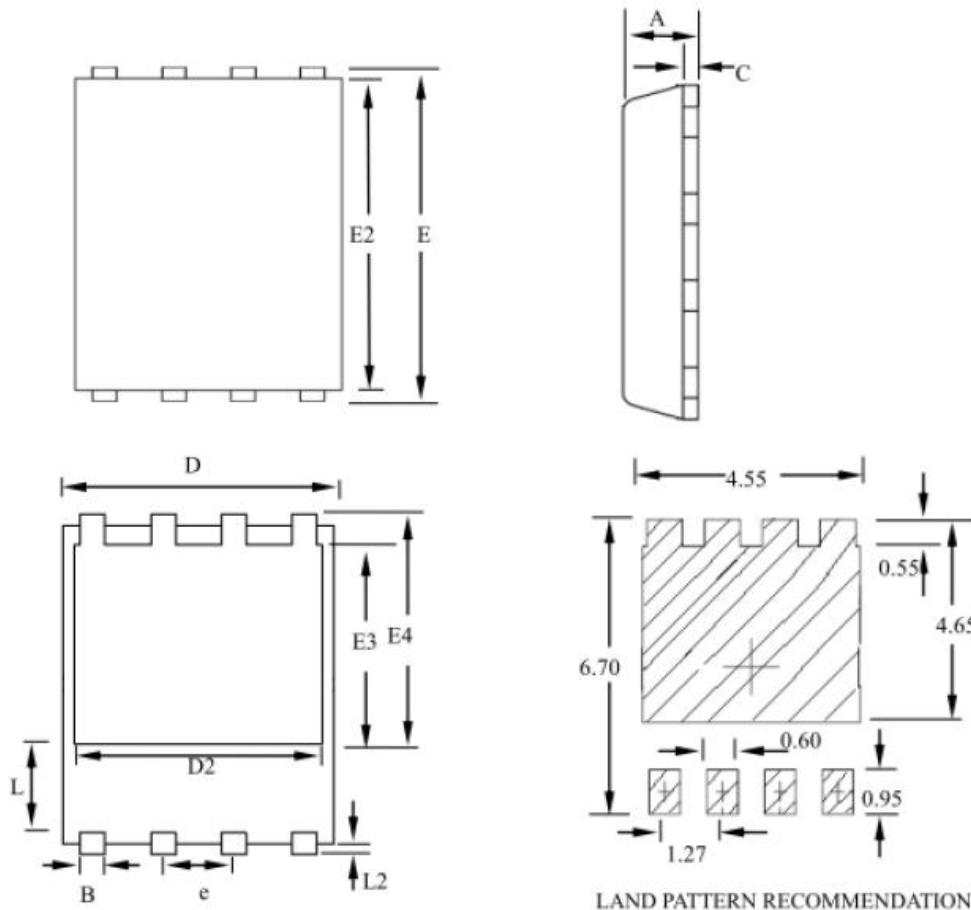


**Figure 10. Maximum Safe Operating Area**





## PRPAK5X6 Package Outline Dimensions



SYMBOLS	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	--	1.20	0.031	--	0.047
B	0.30	--	0.51	0.012	--	0.020
C	0.15	--	0.35	0.006	--	0.014
D	4.80	--	5.30	0.189	--	0.209
D2	3.61	--	4.35	0.142	--	0.171
E	5.90	--	6.35	0.232	--	0.250
E2	5.42	--	5.90	0.213	--	0.232
E3	3.23	--	3.90	0.127	--	0.154
E4	3.69	--	4.55	0.145	--	0.179
L	0.61	--	1.80	0.024	--	0.071
L2	0.05	--	0.36	0.002	--	0.014
e	--	1.27	--	--	0.050	--