

**P-Ch 30V Fast Switching MOSFETs**
**Description**

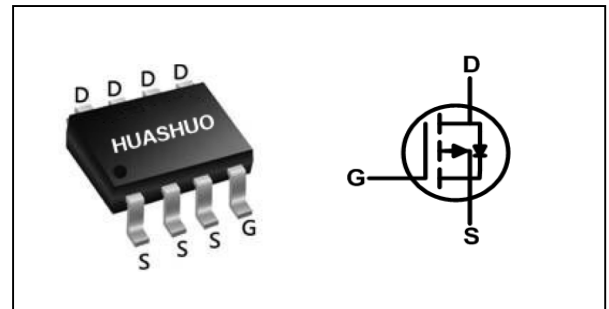
The HSM3031 is the high cell density trenched P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The HSM3031 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

- Super Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

**Product Summary**

$V_{DS}$	-30	V
$R_{DS(ON),max}$	7	m $\Omega$
$I_D$	-16.8	A

**SOP8 Pin Configuration**

**Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D@T_A=25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-16.8	A
$I_D@T_A=100^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-10.6	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	-120	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	80	mJ
$I_{AS}$	Avalanche Current	-40	A
$P_D@T_A=25^\circ C$	Total Power Dissipation <sup>4</sup>	3.1	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

**Thermal Data**

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient <sup>1</sup> ( $t \leq 10S$ )	---	40	$^\circ C/W$
	Thermal Resistance Junction-ambient <sup>1</sup> (Steady State)	---	75	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-case <sup>1</sup>	---	24	$^\circ C/W$



**Electrical Characteristics ( $T_J=25\text{ }^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-30	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=-10V, I_D=-15A$	---	---	7	$m\Omega$
		$V_{GS}=-4.5V, I_D=-10A$	---	---	11	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.2	---	-2.5	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-24V, V_{GS}=0V, T_J=25^\circ C$	---	---	-1	$\mu A$
		$V_{DS}=-24V, V_{GS}=0V, T_J=55^\circ C$	---	---	-5	
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA
$R_g$	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	---	1.2	---	$\Omega$
$Q_g$	Total Gate Charge (-10V)	$V_{DS}=-15V, V_{GS}=-10V, I_D=-15A$	---	60	---	nC
$Q_{gs}$	Gate-Source Charge		---	9	---	
$Q_{gd}$	Gate-Drain Charge		---	15	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=-15V, V_{GS}=-10V, R_G=3.3\Omega, I_D=-10A$	---	17	---	ns
$T_r$	Rise Time		---	40	---	
$T_{d(off)}$	Turn-Off Delay Time		---	55	---	
$T_f$	Fall Time		---	13	---	
$C_{iss}$	Input Capacitance	$V_{DS}=-25V, V_{GS}=0V, f=1MHz$	---	3450	---	$\mu F$
$C_{oss}$	Output Capacitance		---	255	---	
$C_{rss}$	Reverse Transfer Capacitance		---	140	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current <sup>1,5</sup>	$V_G=V_D=0V$ , Force Current	---	---	-16.8	A
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0V, I_S=-1A, T_J=25^\circ C$	---	---	-1.2	V
$t_{rr}$	Reverse Recovery Time	$I_F=-20A, di/dt=100A/\mu s,$	---	22	---	nS
$Q_{rr}$	Reverse Recovery Charge	$T_J=25^\circ C$	---	72	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{DD}=-50V, V_{GS}=-10V, L=0.1mH, I_{AS}=-40A$
- 4.The power dissipation is limited by 150 $^\circ C$  junction temperature
- 5.The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.



Typical Characteristics

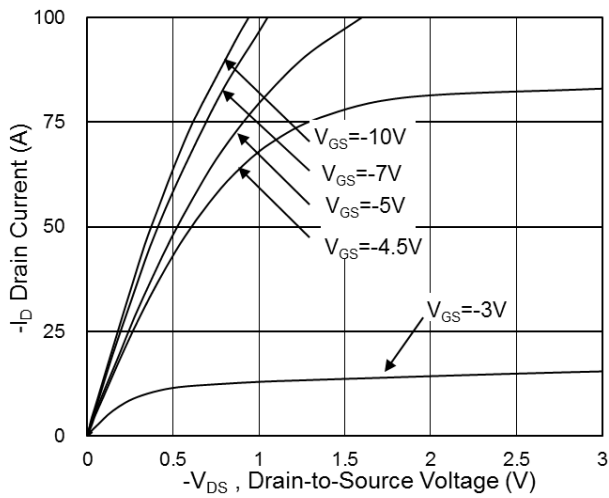


Fig.1 Typical Output Characteristics

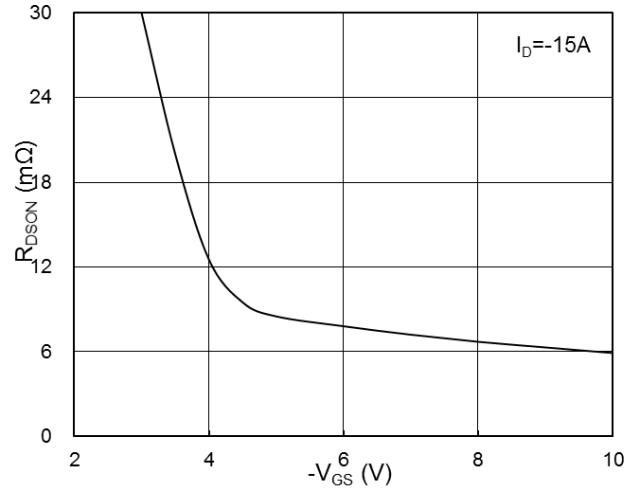


Fig.2 On-Resistance vs. Gate-Source Voltage

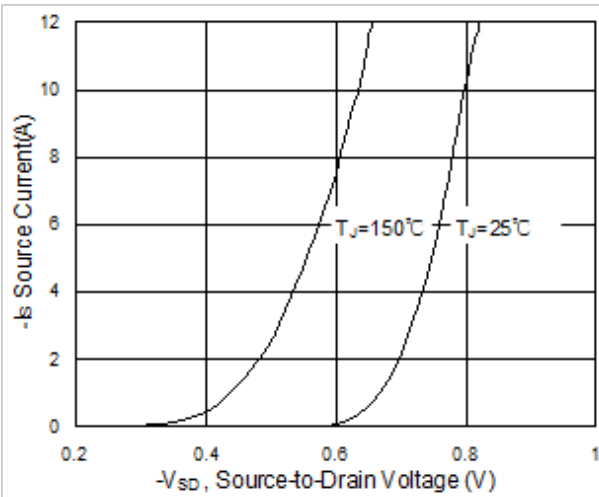


Fig.3 Forward Characteristics of Reverse

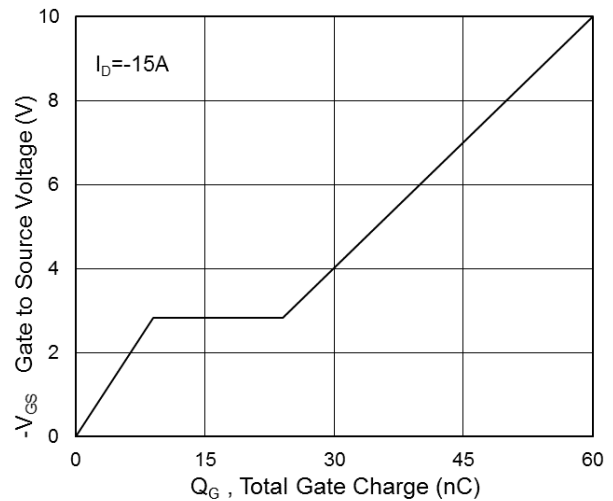


Fig.4 Gate-Charge Characteristics

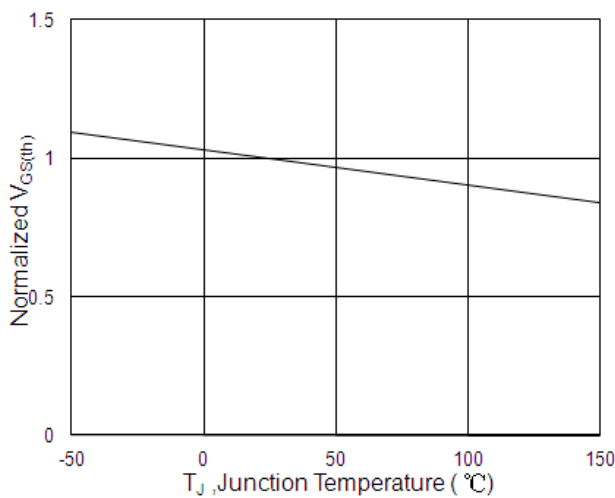


Fig.5 Normalized  $-V_{GS(th)}$  vs.  $T_J$

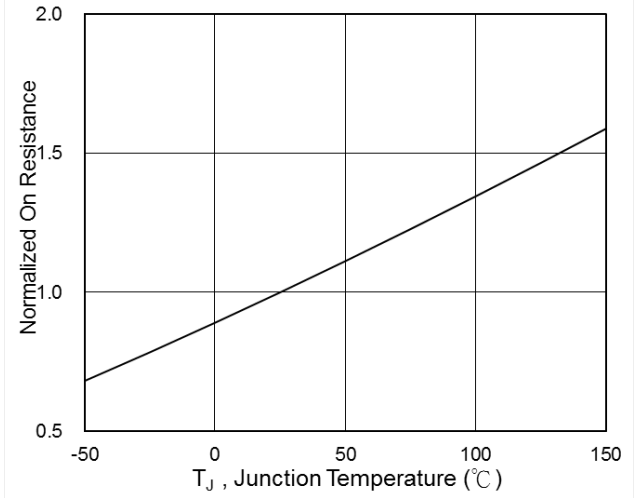
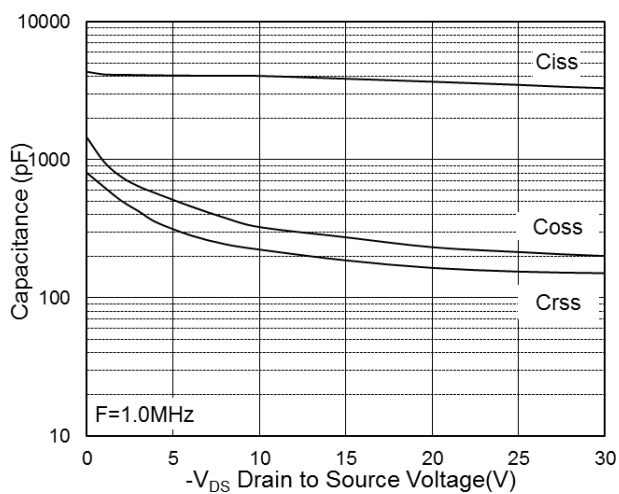


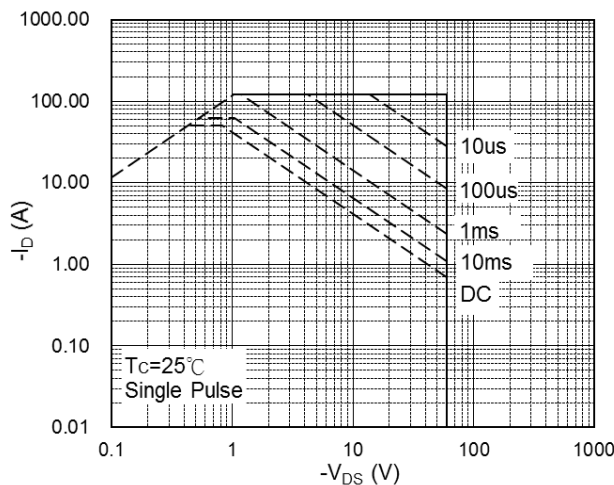
Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$



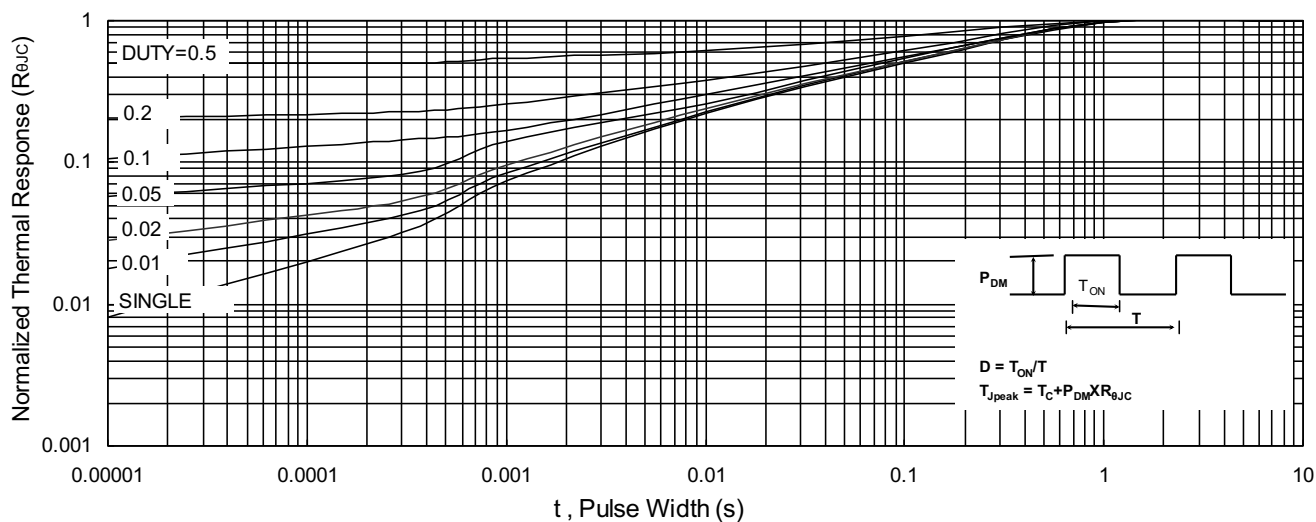
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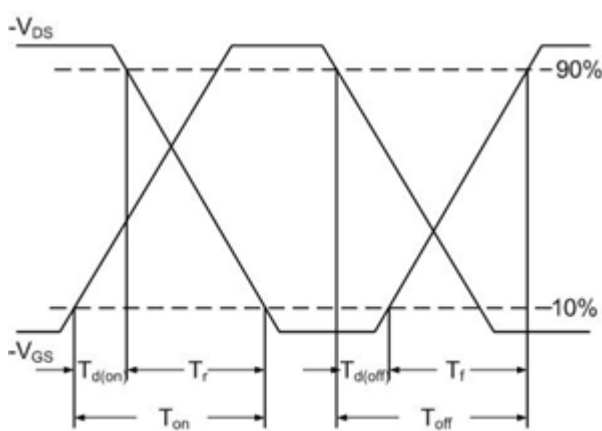
**Fig.7 Capacitance**



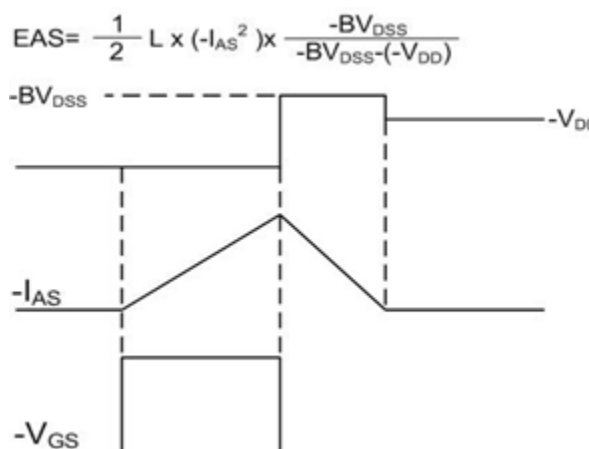
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



**Fig.10 Switching Time Waveform**



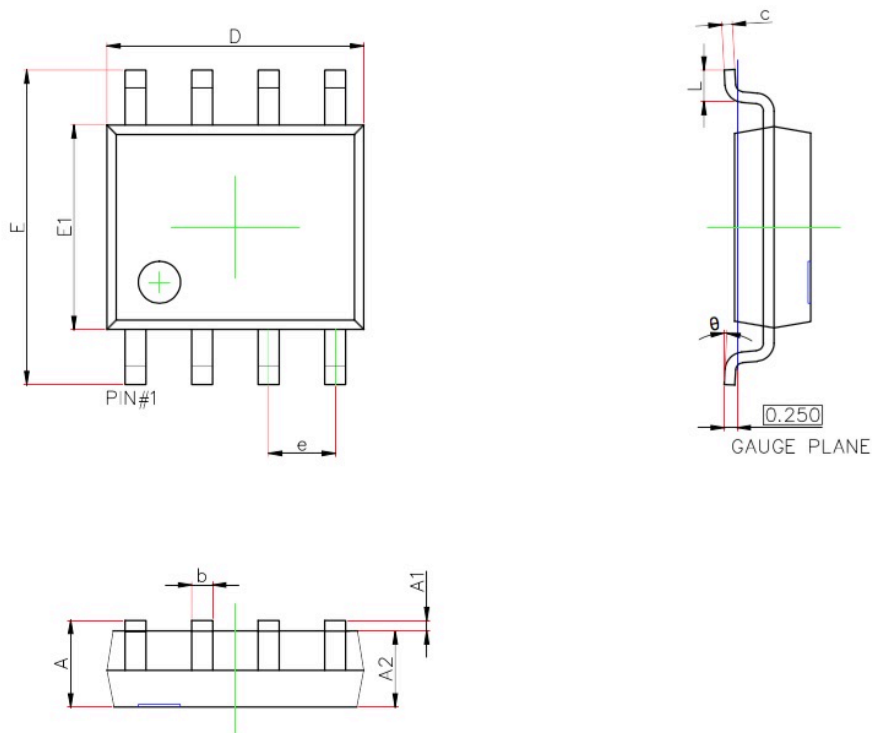
**Fig.11 Unclamped Inductive Switching**



## Ordering Information

Part Number	Package code	Packaging
HSM3031	SOP-8	2500/Tape&Reel

SOP8PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.800	5.000	0.189	0.197
e	1.270 (BSC)		0.050 (BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°