

N-Ch and P-Ch Fast Switching MOSFETs
Description

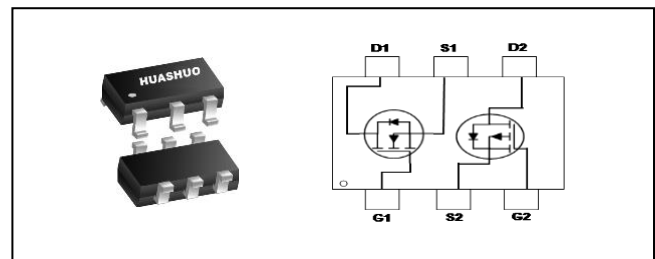
The HSW6604 is the high performance complementary N-ch and P-ch MOSFETs with high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The HSW6604 meet the RoHS and Green Product requirement with full function reliability approved.

- Green Device Available
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Advanced high cell density Trench technology

Product Summary

BVDSS	RDSON	ID
20V	38mΩ	4A
-20V	64mΩ	-3A

SOT23-6L Pin Configuration

Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-Channel	P-Channel	
		Steady State	Steady State	
V _{DS}	Drain-Source Voltage	20	-20	V
V _{GS}	Gate-Source Voltage	±12	±12	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ -4.5V ¹	4	-3	A
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ -4.5V ¹	3	-2.6	A
I _{DM}	Pulsed Drain Current ²	16	-12.8	A
P _D @T _A =25°C	Total Power Dissipation ³	1.1	1.1	W
T _{STG}	Storage Temperature Range	-55 to 150	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹	---	100	°C/W

N-Ch and P-Ch Fast Switching MOSFETs
N-Channel Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
B _{VDSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	20	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =4.5V, I _D =3A	---	38	48	mΩ
		V _{GS} =2.5V, I _D =2A	---	45	55	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	0.5	0.75	1.2	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =16V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =16V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±12V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =3A	---	5	---	S
Q _g	Total Gate Charge (4.5V)	V _{DS} =10V, V _{GS} =4.5V, I _D =3A	---	2.7	---	nC
Q _{gs}	Gate-Source Charge		---	0.4	---	
Q _{gd}	Gate-Drain Charge		---	0.5	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =10V, V _{GS} =4.5V, R _G =3.3Ω I _D =3A	---	2.3	---	ns
T _r	Rise Time		---	3.1	---	
T _{d(off)}	Turn-Off Delay Time		---	21	---	
T _f	Fall Time		---	2.6	---	
C _{iss}	Input Capacitance	V _{DS} =10V, V _{GS} =0V, f=1MHz	---	240	---	pF
C _{oss}	Output Capacitance		---	49	---	
C _{rss}	Reverse Transfer Capacitance		---	23	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	1.8	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

N-Ch and P-Ch Fast Switching MOSFETs

N-Channel Typical Characteristics

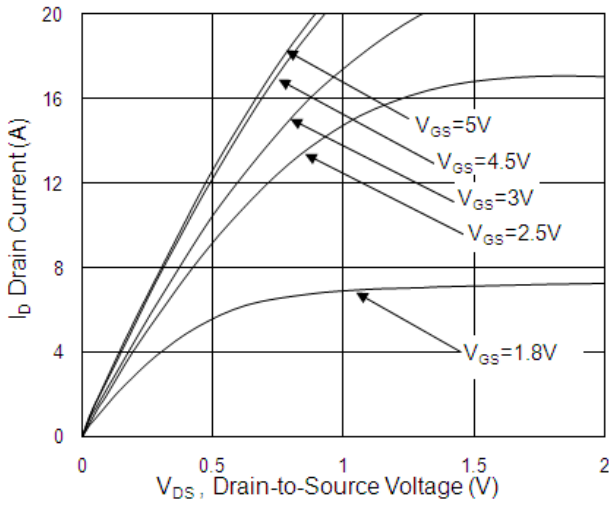


Fig.1 Typical Output Characteristics

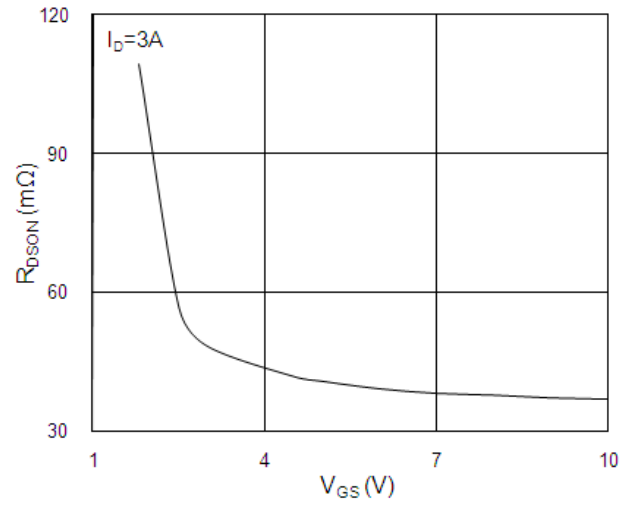


Fig.2 On-Resistance vs G-S Voltage

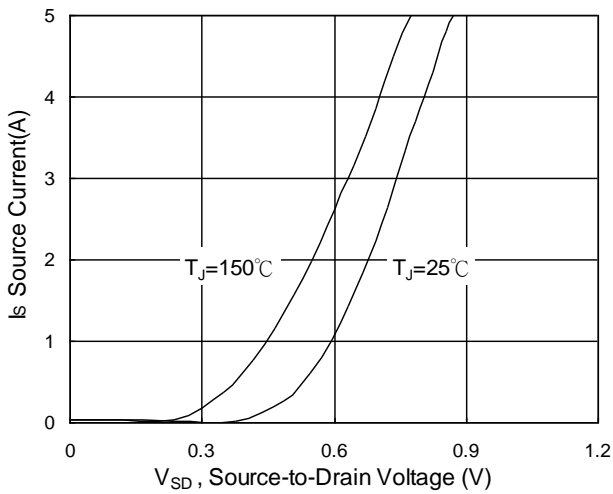


Fig.3 Source Drain Forward Characteristics

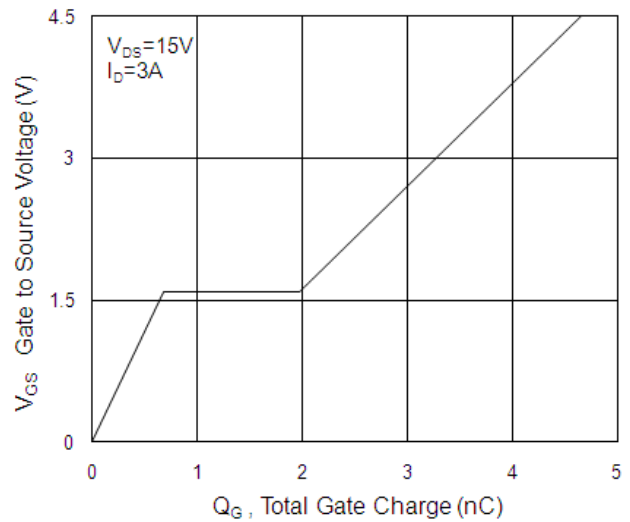


Fig.4 Gate-Charge Characteristics

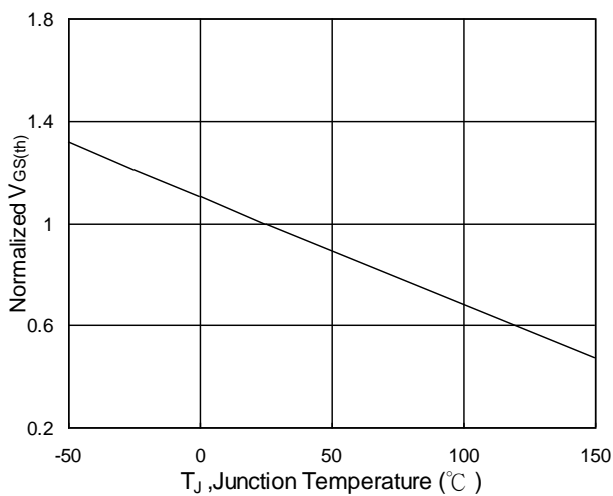


Fig.5 Normalized $V_{GS(th)}$ vs T_J

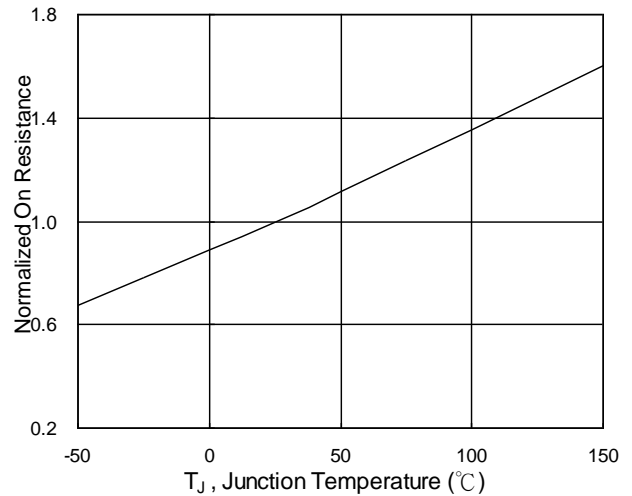


Fig.6 Normalized $R_{DS(on)}$ vs T_J

N-Ch and P-Ch Fast Switching MOSFETs

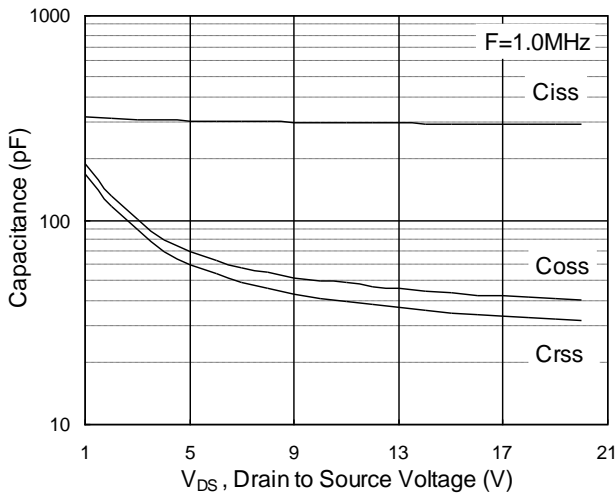


Fig.7 Capacitance

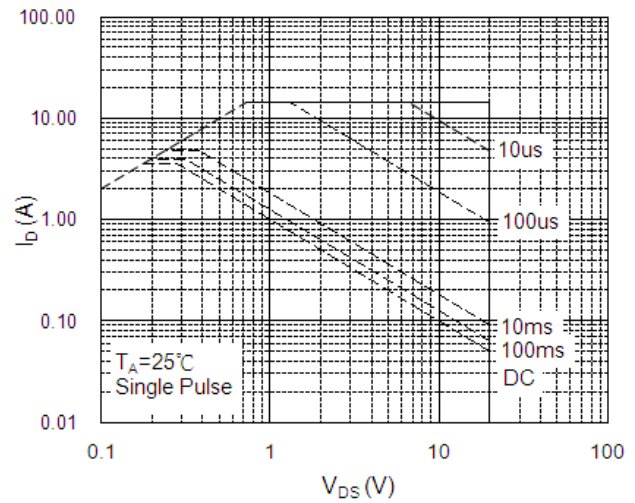


Fig.8 Safe Operating Area

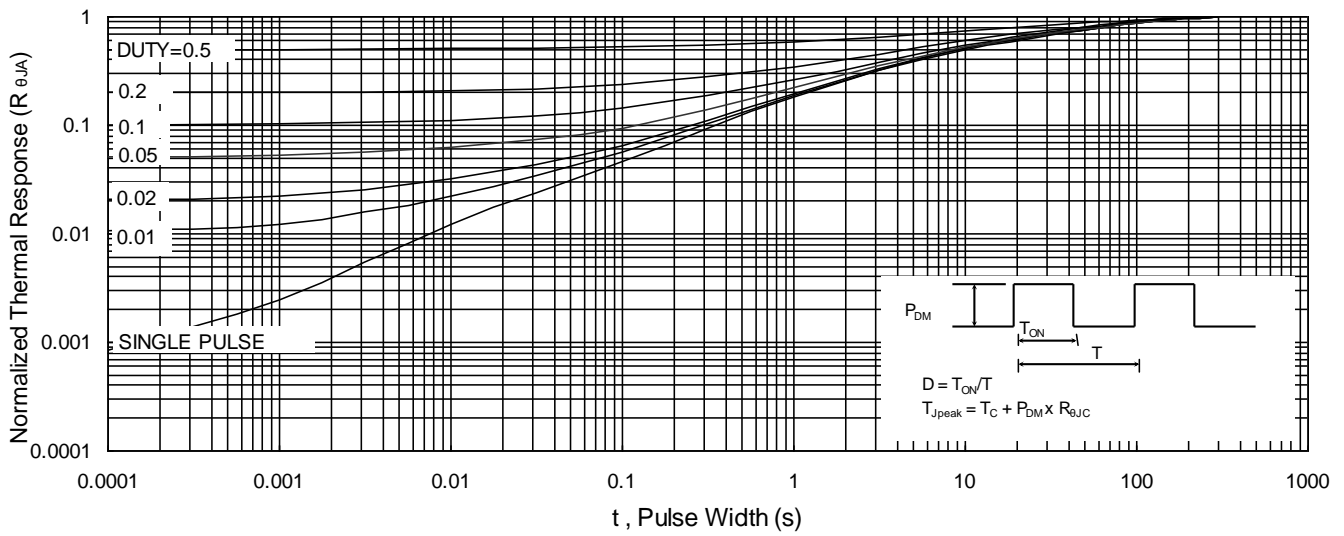


Fig.9 Normalized Maximum Transient Thermal Impedance

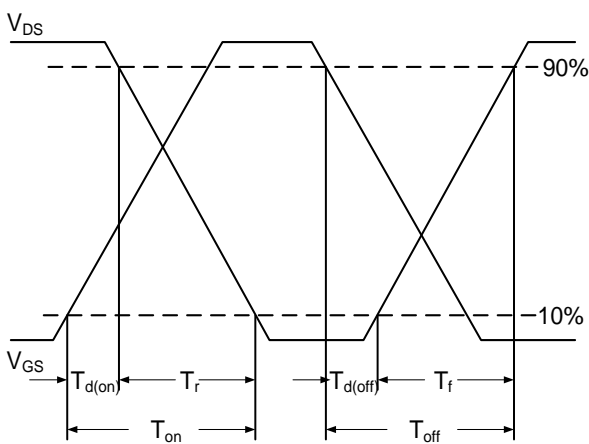


Fig.10 Switching Time Waveform

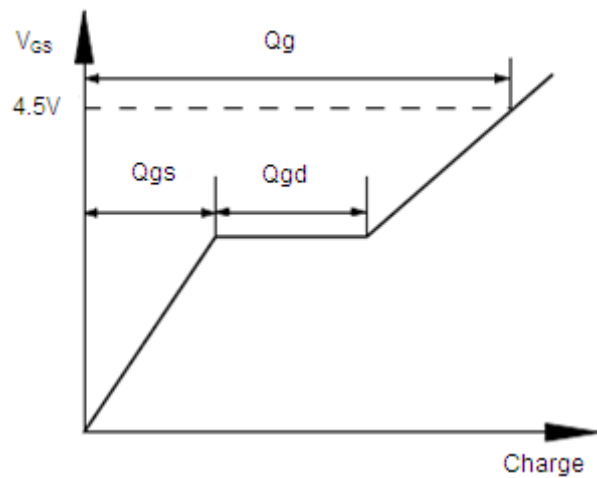


Fig.11 Gate Charge Waveform

N-Ch and P-Ch Fast Switching MOSFETs
P-Channel Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
B _V DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-20	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-4.5V, I _D =-3A	---	64	80	mΩ
		V _{GS} =-2.5V, I _D =-2A	---	82	120	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-0.4	-0.65	-1.2	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-16V, V _{GS} =0V, T _J =25°C	---	---	-1	uA
		V _{DS} =-16V, V _{GS} =0V, T _J =55°C	---	---	-5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±12V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =-5V, I _D =-3A	---	5	---	S
Q _g	Total Gate Charge (-4.5V)	V _{DS} =-10V, V _{GS} =-4.5V, I _D =-3A	---	6.1	---	nC
Q _{gs}	Gate-Source Charge		---	1.7	---	
Q _{gd}	Gate-Drain Charge		---	1.2	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =-10V, V _{GS} =-4.5V, R _G =3.3Ω I _D =-3A	---	12	---	ns
T _r	Rise Time		---	6.6	---	
T _{d(off)}	Turn-Off Delay Time		---	113	---	
T _f	Fall Time		---	46	---	
C _{iss}	Input Capacitance	V _{DS} =-10V, V _{GS} =0V, f=1MHz	---	561	---	pF
C _{oss}	Output Capacitance		---	61	---	
C _{rss}	Reverse Transfer Capacitance		---	52	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _s	Continuous Source Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	-1.8	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =-1A, T _J =25°C	---	-0.81	-1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

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Typical Characteristics

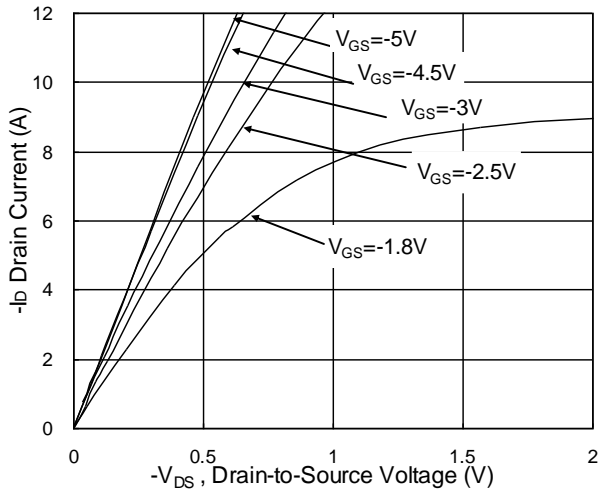


Fig.1 Typical Output Characteristics

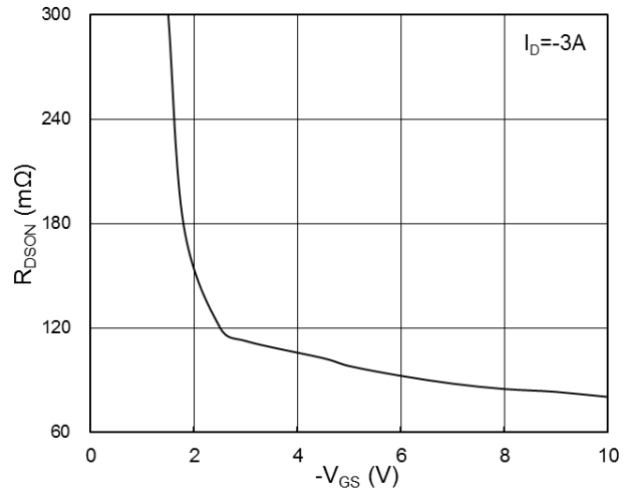


Fig.2 On-Resistance vs G-S Voltage

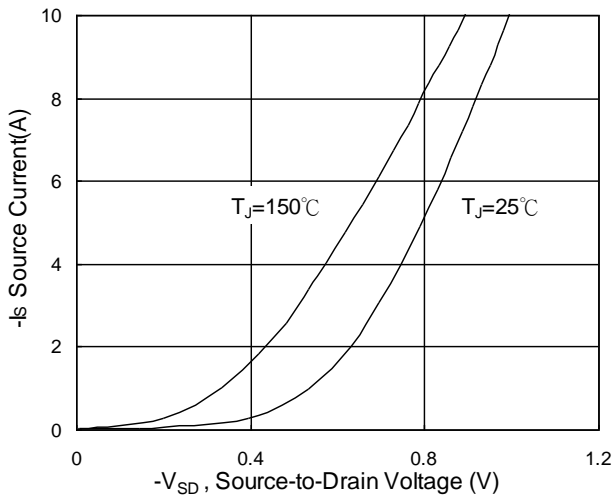


Fig.3 Source Drain Forward Characteristics

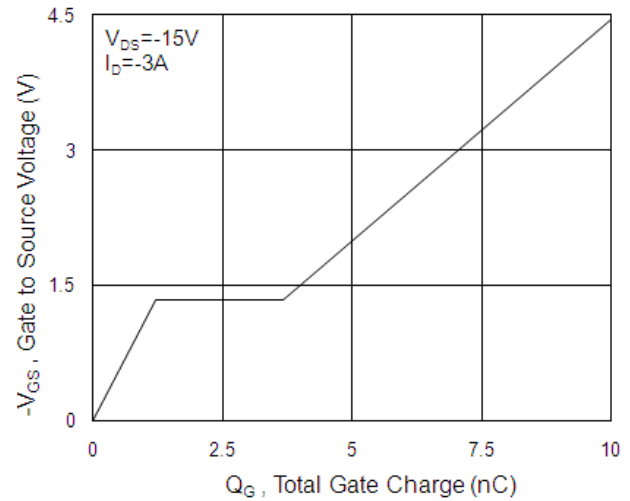


Fig.4 Gate-Charge Characteristics

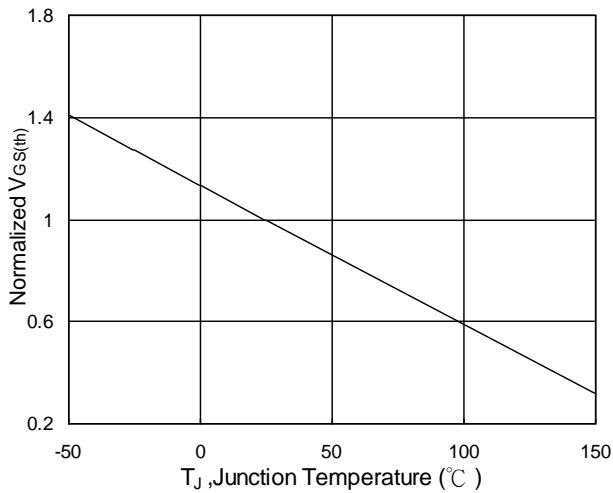


Fig.5 Normalized $V_{GS(th)}$ vs T_J

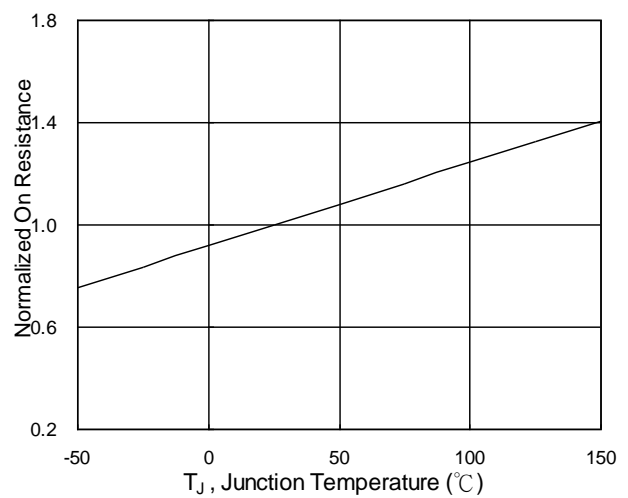


Fig.6 Normalized $R_{DS(on)}$ vs T_J

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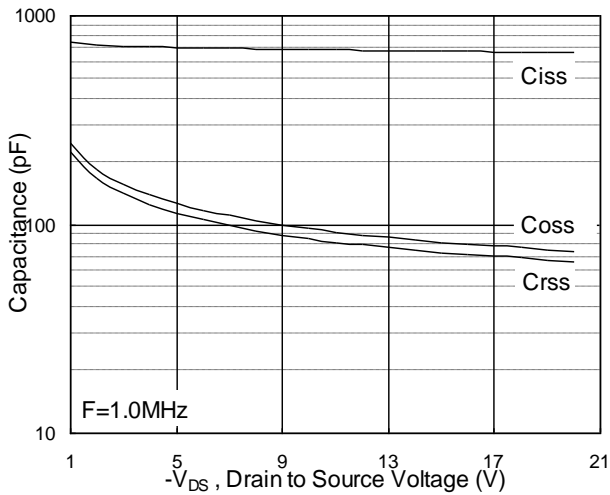


Fig.7 Capacitance

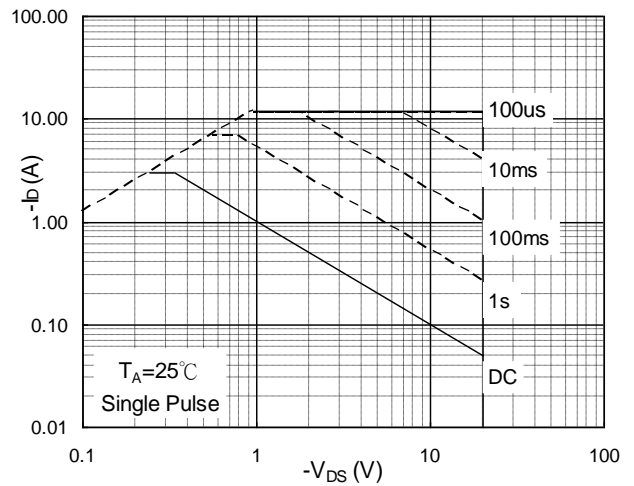


Fig.8 Safe Operating Area

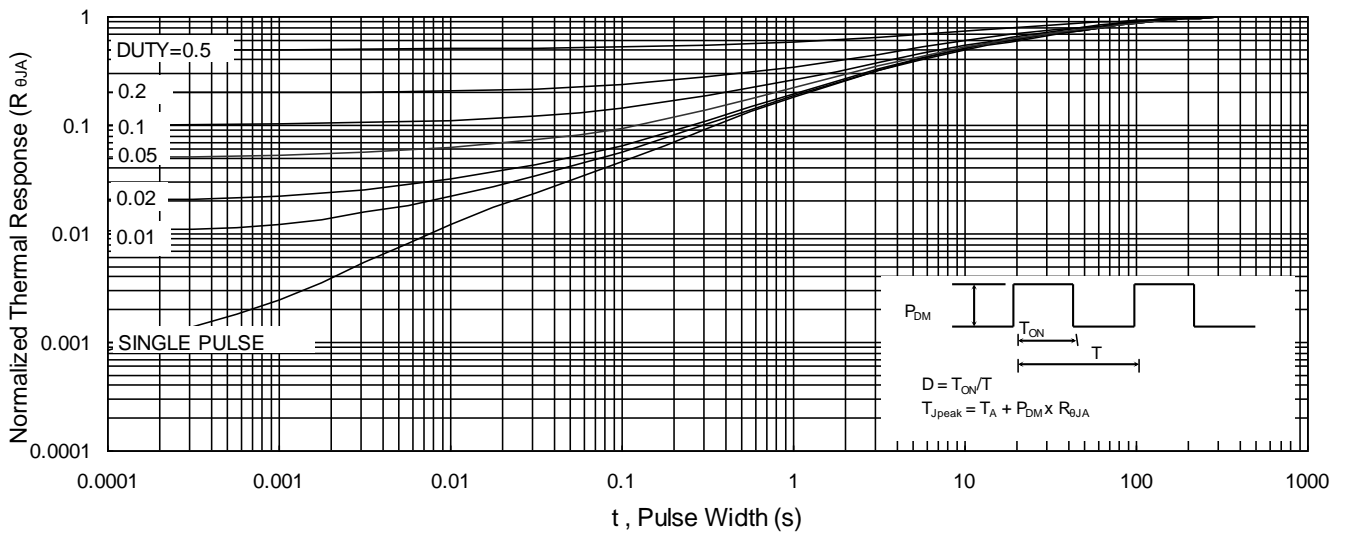


Fig.9 Normalized Maximum Transient Thermal Impedance

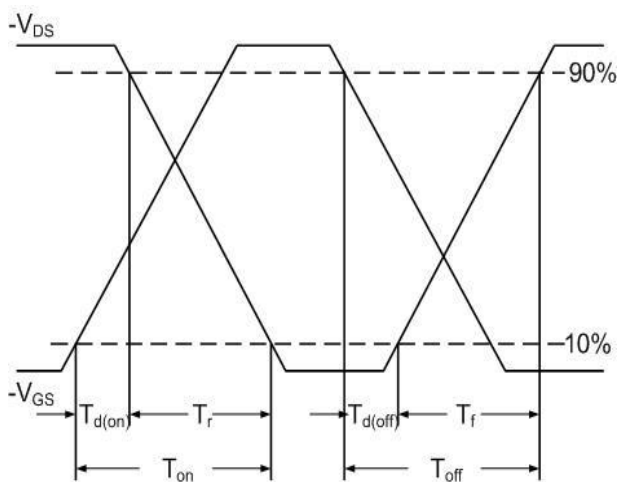


Fig.10 Switching Time Waveform

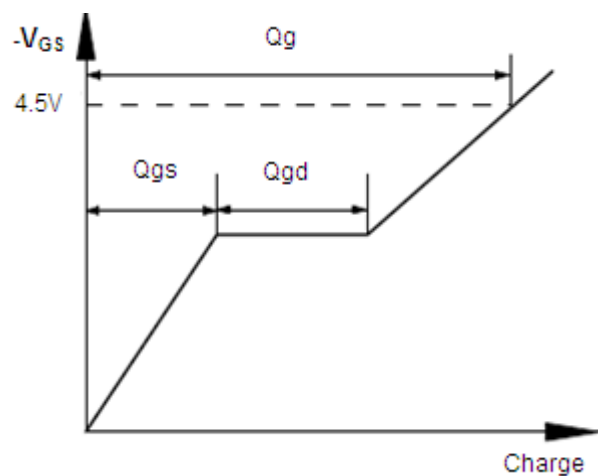
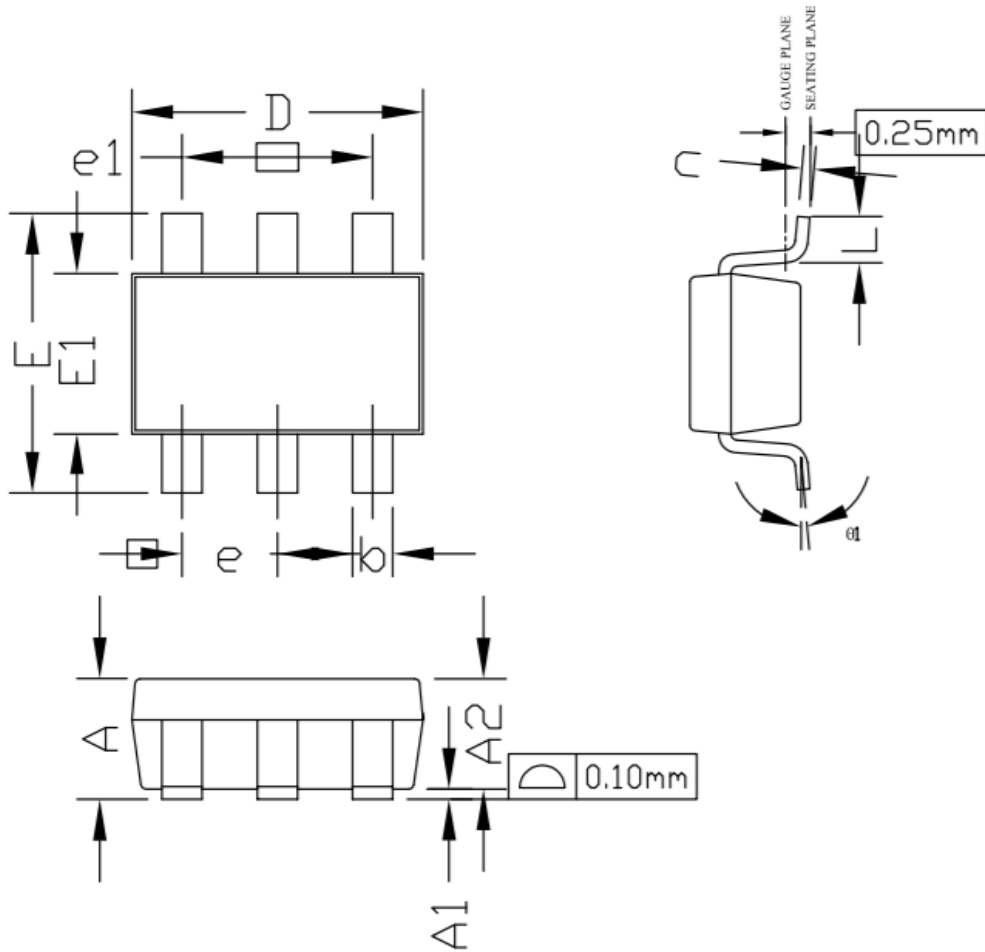
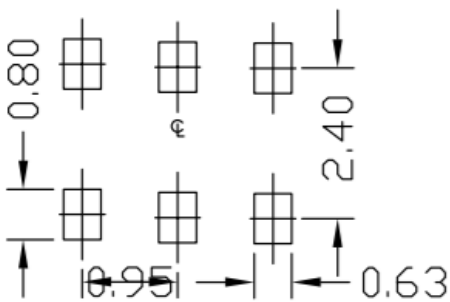


Fig.11 Gate Charge Waveform

SOT23-6 Package Outline Dimensions



RECOMMENDED LAND PATTERN



UNIT: mm

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	—	1.25	0.031	—	0.049
A1	0.00	—	0.15	0.000	—	0.006
A2	0.70	1.10	1.20	0.028	0.043	0.047
b	0.30	0.40	0.50	0.012	0.016	0.020
c	0.08	0.13	0.20	0.003	0.005	0.008
D	2.70	2.90	3.10	0.106	0.114	0.122
E	2.50	2.80	3.10	0.098	0.110	0.122
E1	1.50	1.60	1.70	0.059	0.063	0.067
e	0.95 BSC.			0.037BSC.		
e1	1.90 BSC.			0.075 BSC.		
L	0.30	—	0.60	0.012	—	0.024
θ_1	0°	—	8°	0°	—	8°