



Description

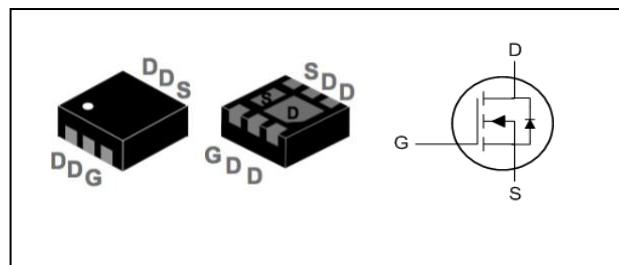
The HSCB2012 is the high cell density trenched N-ch MOSFETs, which provides excellent RDSON and efficiency for most of the small power switching and load switch applications.

The HSCB2012 meet the RoHS and Green Product requirement with full function reliability approved.

Product Summary

V _{DS}	20	V
R _{DS(ON),typ}	12	mΩ
I _D	12	A

DFN2*2-6L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	20	V
V _{GS}	Gate-Source Voltage	±12	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 4.5V ₁	12	A
I _{DM}	Pulsed Drain Current ₂	48	A
P _D @T _A =25°C	Total Power Dissipation ₃	3.3	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ₁	100	°C/W



Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_D=250\mu\text{A}$	20	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $\text{I}_D=1\text{mA}$	---	0.018	---	$\text{V}/^\circ\text{C}$
$\text{R}_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance ₂	$\text{V}_{\text{GS}}=4.5\text{V}$, $\text{I}_D=12\text{A}$	---	12	15	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=2.5\text{V}$, $\text{I}_D=8\text{A}$	---	15	20	
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$, $\text{I}_D=250\mu\text{A}$	0.5	0.7	0.9	V
$\Delta \text{V}_{\text{GS}(\text{th})}$	$\text{V}_{\text{GS}(\text{th})}$ Temperature Coefficient		---	-3.1	---	$\text{mV}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=16\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$\text{V}_{\text{DS}}=16\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 12\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$\text{V}_{\text{DS}}=5\text{V}$, $\text{I}_D=12\text{A}$	---	10	---	S
Q_g	Total Gate Charge (4.5V)	$\text{V}_{\text{DS}}=10\text{V}$, $\text{V}_{\text{GS}}=4.5\text{V}$, $\text{I}_D=12\text{A}$	---	13	---	nC
Q_{gs}	Gate-Source Charge		---	2.2	---	
Q_{gd}	Gate-Drain Charge		---	1	---	
$\text{T}_{\text{d}(\text{on})}$	Turn-On Delay Time	$\text{V}_{\text{DS}}=10\text{V}$, $\text{V}_{\text{GS}}=4.5\text{V}$, $\text{R}_G=3.3\Omega$ $\text{I}_D=4\text{A}$	---	9	---	ns
T_r	Rise Time		---	11	---	
$\text{T}_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	33	---	
T_f	Fall Time		---	36	---	
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=10\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	960	---	pF
C_{oss}	Output Capacitance		---	240	---	
C_{rss}	Reverse Transfer Capacitance		---	110	---	

Diode Characteristics

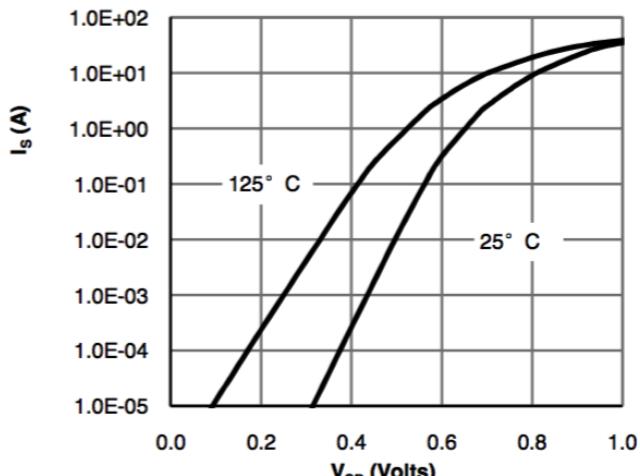
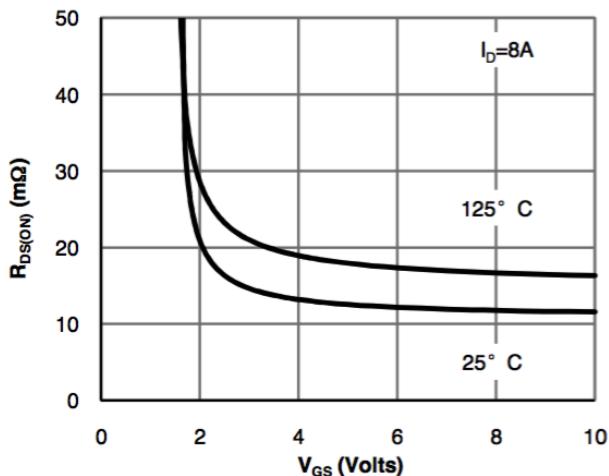
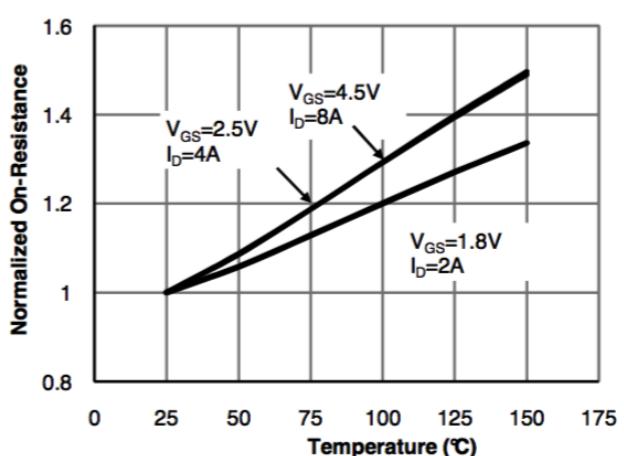
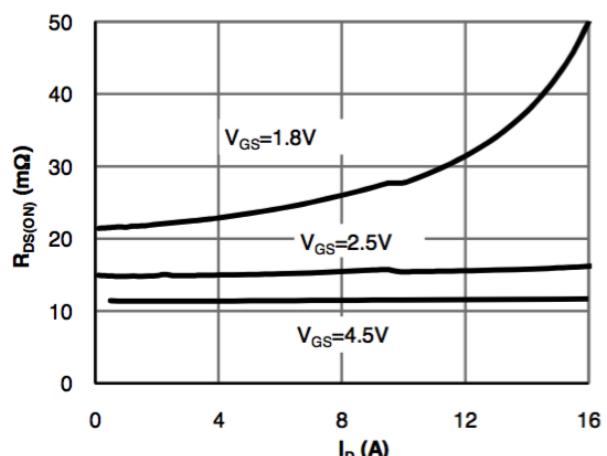
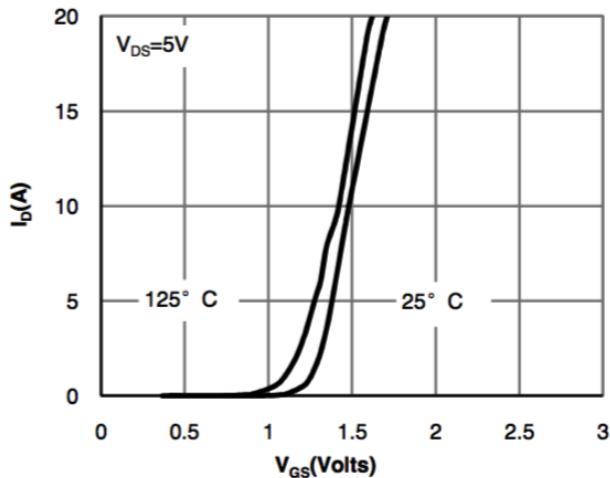
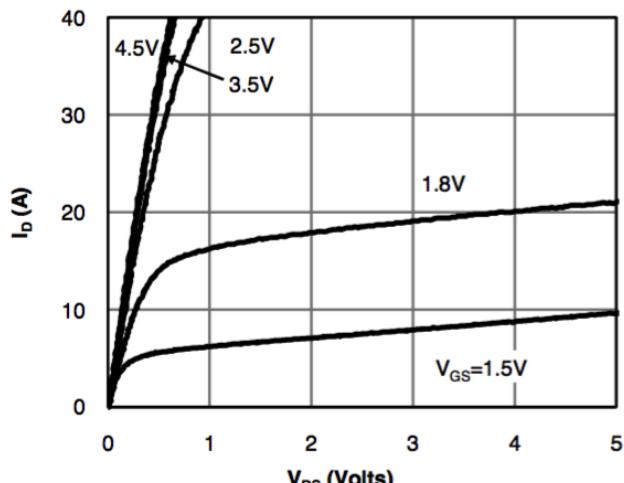
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current _{1,4}	$\text{V}_G=\text{V}_D=0\text{V}$, Force Current	---	---	12	A
I_{SM}	Pulsed Source Current _{2,4}		---	---	48	A
V_{SD}	Diode Forward Voltage ₂	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.



Typical Characteristics





N-Ch 20V Fast Switching MOSFETs

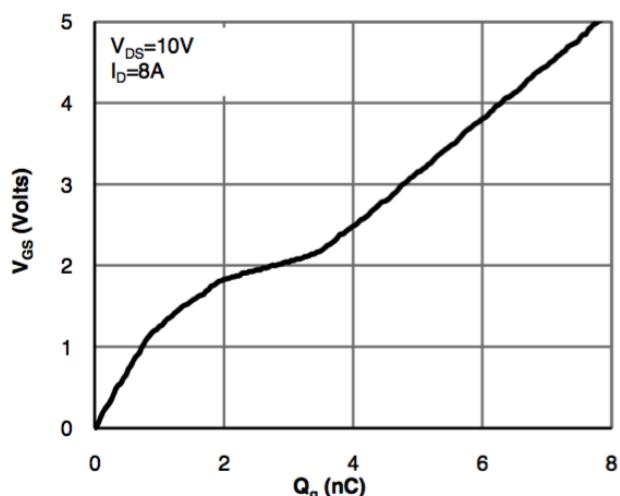


Figure 7: Gate-Charge Characteristics

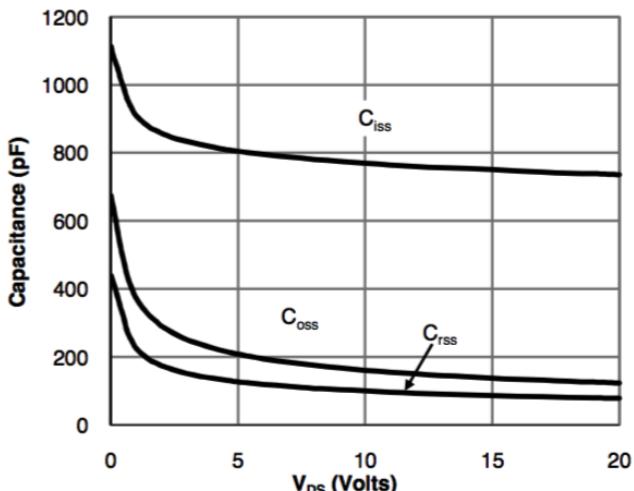


Figure 8: Capacitance Characteristics

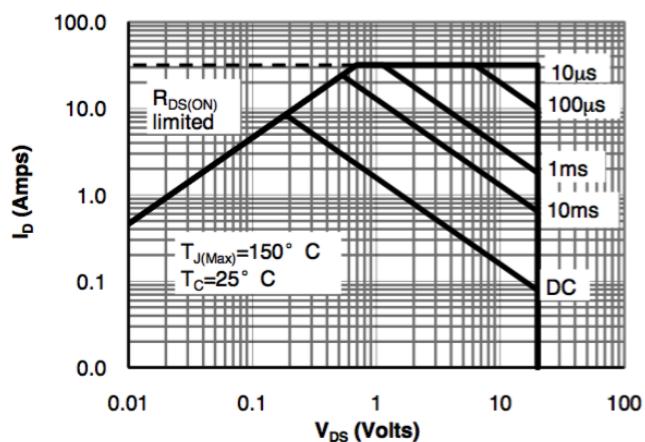


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

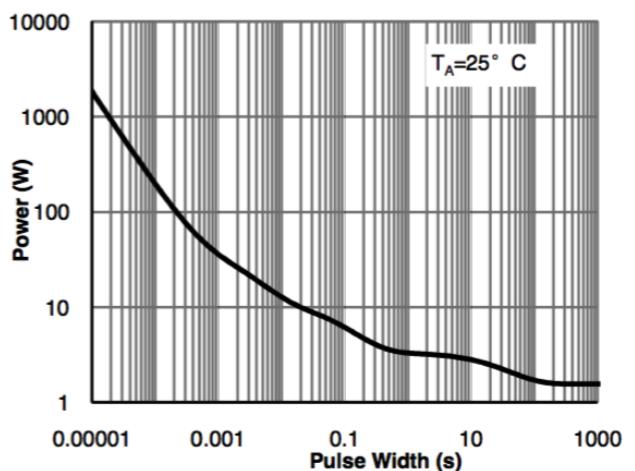


Figure 11: Single Pulse Power Rating Junction-to-Ambient (Note H)

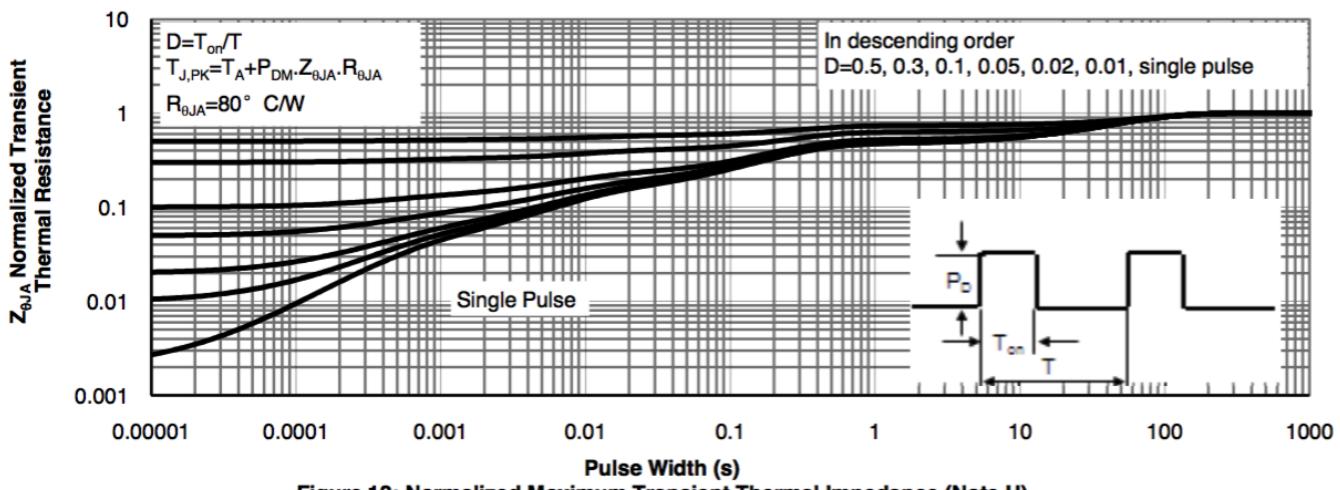
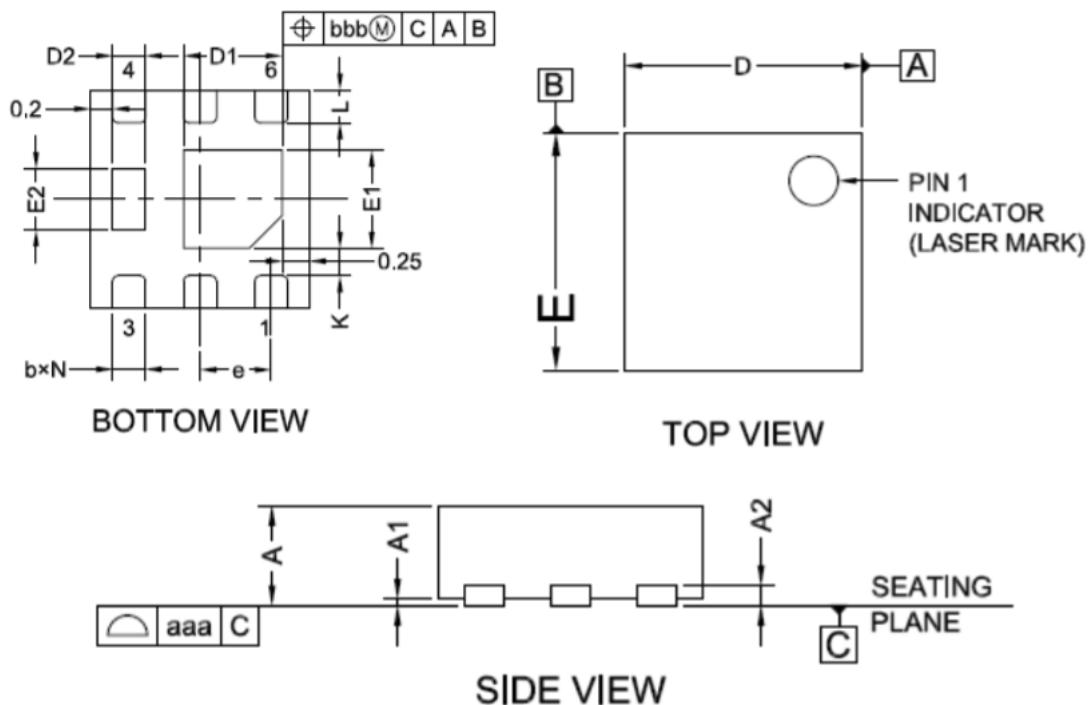


Figure 12: Normalized Maximum Transient Thermal Impedance (Note H)



DFN2x2-6L 2EP PACKAGE INFORMATION



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX
A	0.50	0.55	0.60
A1	0.00	0.02	0.05
A2 0.152REF.			
b	0.25	0.30	0.35
D	1.95	2.00	2.05
D1	0.80	0.90	1.00
D2	0.25	0.30	0.35
E	1.95	2.00	2.05
E1	0.80	0.90	1.00
E2	0.46	0.56	0.66
e 0.65BSC			
L	0.25	0.30	0.35
J	0.40BSC		
K	0.20MIN		
N	6		
aaa	0.08		
bbb	0.10		