



Description

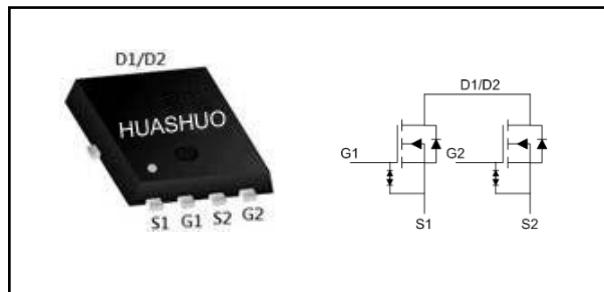
The HSBE2738 is the low RDSON trenched N-CH MOSFETs with robust ESD protection. This product is suitable for Lithium-ion battery pack applications.

The HSBE2738 meet the RoHS and Green Product requirement with full function reliability approved.

Product Summary

V _{DS}	20	V
R _{DS(ON),max}	9.5	mΩ
I _D	12	A

PRPAK3X3 NEP Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	20	V
V _{GS}	Gate-Source Voltage	±12	V
I _D @T _A =25°C	Continuous Drain Current ₁	12	A
I _D @T _A =70°C	Continuous Drain Current ₁	9.6	A
I _{DM}	Pulsed Drain Current ₂	72	A
P _D @T _A =25°C	Total Power Dissipation ₃	1.47	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ₁	95	°C/W



N-Ch 20V Fast Switching MOSFETs

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	20	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=1\text{mA}$	---	0.014	---	$\text{V}/^\circ\text{C}$
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=3\text{A}$	---	8	9.5	$\text{m}\Omega$
		$V_{\text{GS}}=4.0\text{V}$, $I_{\text{D}}=3\text{A}$	---	8.5	9.8	$\text{m}\Omega$
		$V_{\text{GS}}=3.1\text{V}$, $I_{\text{D}}=3\text{A}$	---	10.5	12.5	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}$, $I_{\text{D}}=3\text{A}$	---	12	15	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=250\mu\text{A}$	0.5	---	1.5	V
$\Delta V_{\text{GS(th)}}$	$V_{\text{GS(th)}}$ Temperature Coefficient		---	-2.09	---	$\text{mV}/^\circ\text{C}$
I_{DS}	Drain-Source Leakage Current	$V_{\text{DS}}=16\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
I_{GS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 8\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 5	uA
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	1.83	---	Ω
Q_g	Total Gate Charge (4.5V)	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=10\text{A}$	---	13	---	nC
Q_{gs}	Gate-Source Charge		---	2.3	---	
Q_{gd}	Gate-Drain Charge		---	7.2	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=15\text{V}$, $V_{\text{GS}}=4.5\text{V}$, $R_g=6\Omega$, $I_{\text{D}}=6\text{A}$	---	22	---	ns
T_r	Rise Time		---	85	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	125	---	
T_f	Fall Time		---	46	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=10\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	735	---	pF
C_{oss}	Output Capacitance		---	256	---	
C_{rss}	Reverse Transfer Capacitance		---	230	---	

Diode Characteristics

Symbol	Parameter	Conditions	Max.	Unit
I_s	Continuous Source Current ^{1,6}	$V_G=V_D=0\text{V}$, Force Current	12	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature.
- 4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.



Typical Characteristics

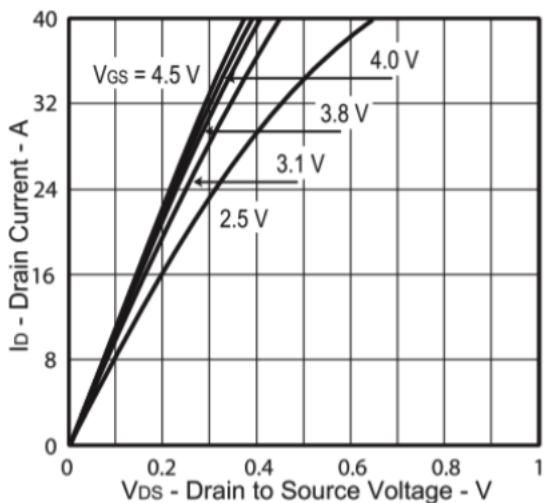


Fig.1 Typical Output Characteristics

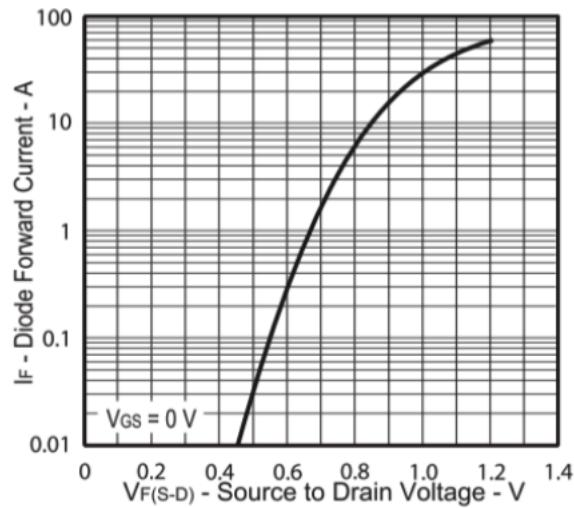


Fig.3 Forward Characteristics of Reverse

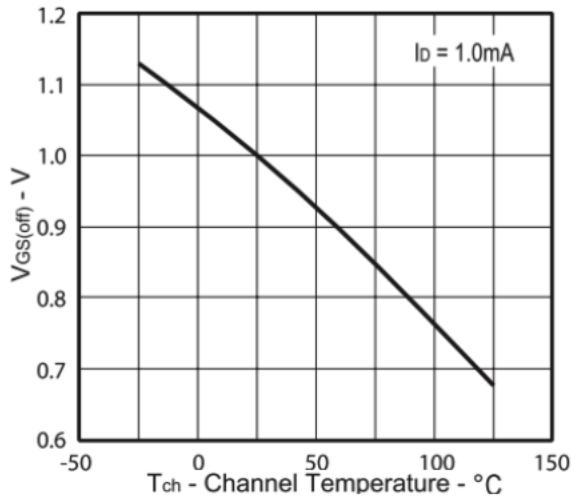


Fig.5 $V_{GS(th)}$ vs. T_{CH}

N-Ch 20V Fast Switching MOSFETs

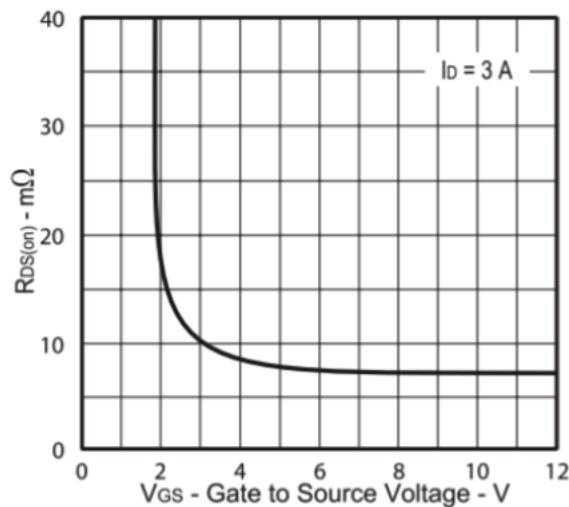


Fig.2 On-Resistance vs. Gate-Source Voltage

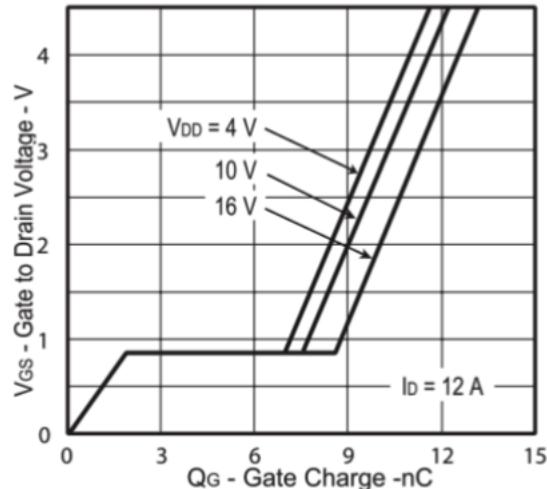


Fig.4 Gate-Charge Characteristics

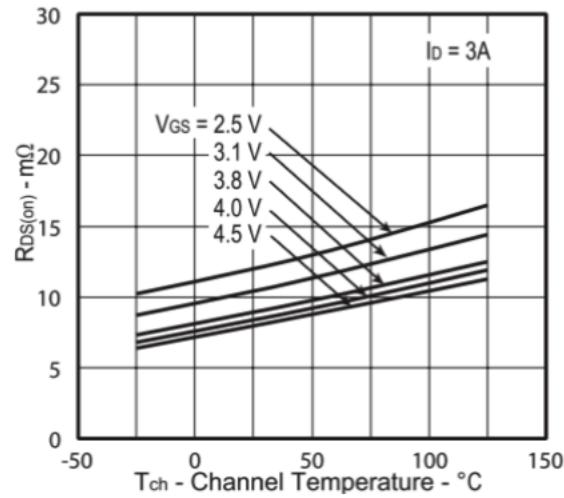


Fig.6 Normalized $R_{DS(on)}$ vs. T_{CH}



N-Ch 20V Fast Switching MOSFETs

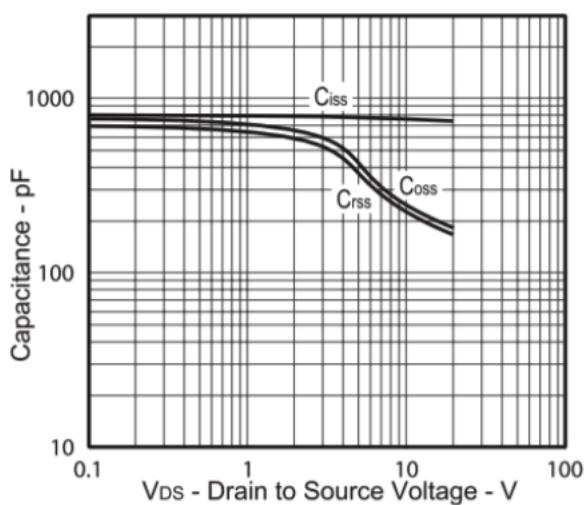


Fig.7 Capacitance

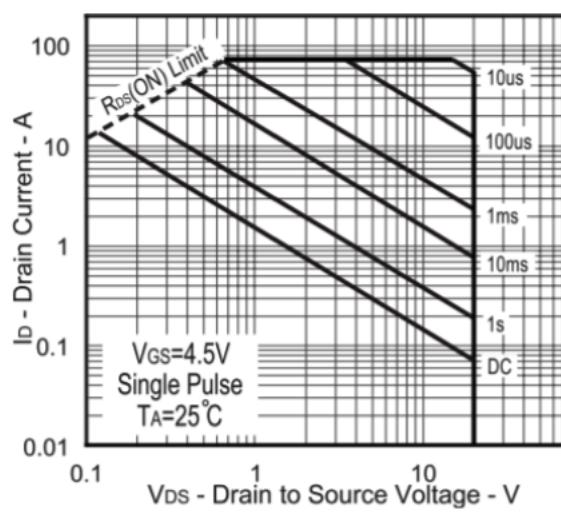


Fig.8 Safe Operating Area

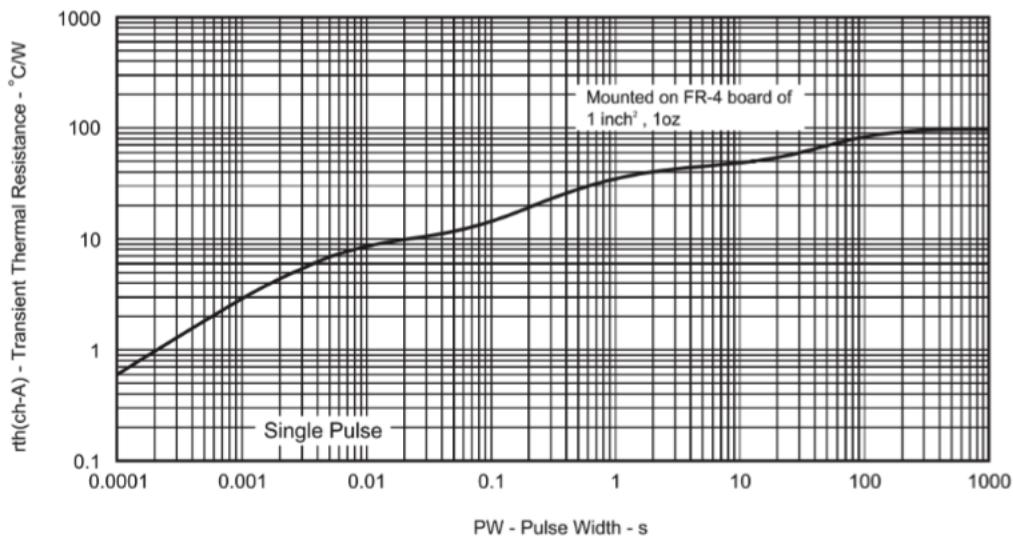


Fig.9 Normalized Maximum Transient Thermal Impedance

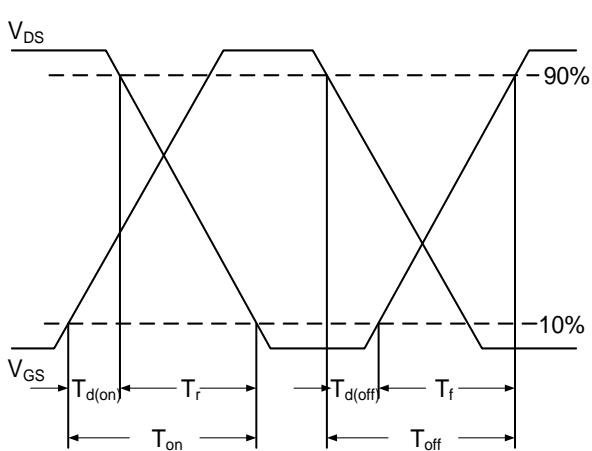


Fig.10 Switching Time Waveform

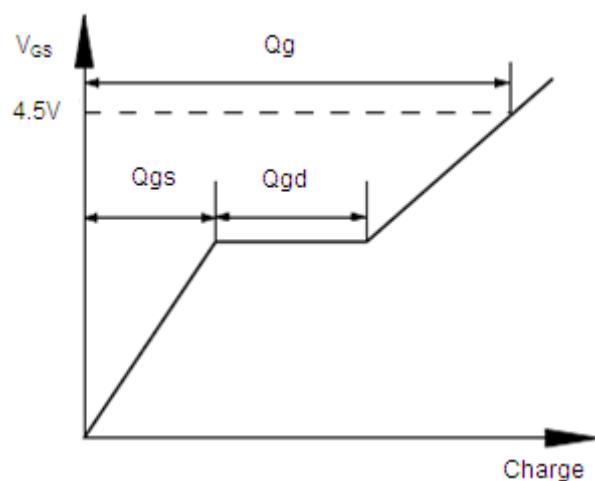
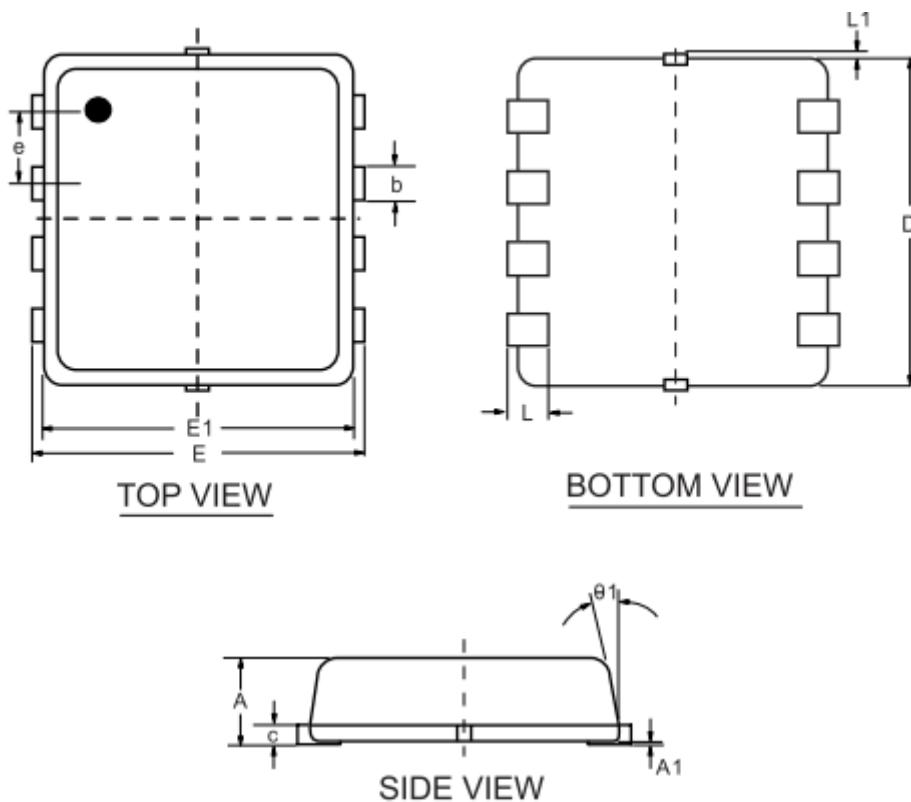


Fig.11 Gate Charge Waveform



N-Ch 20V Fast Switching MOSFETs

PRPAK3X3 NEP Package Outline Dimensions



SYMBOLS	MILLIMETERS		
	MIN	NOM	MAX
A	0.700	0.800	0.900
A1	0.000	—	0.050
b	0.240	0.300	0.350
c	0.080	0.152	0.250
D	2.800	2.900	3.000
E	2.700	2.800	2.900
E1	2.200	2.300	2.400
e	0.650 BSC		
L	0.200	0.375	0.450
L1	0.000	—	0.100
θ1	0°	10°	12°